Instruction Set Architecture of IA-32 Privilege Levels

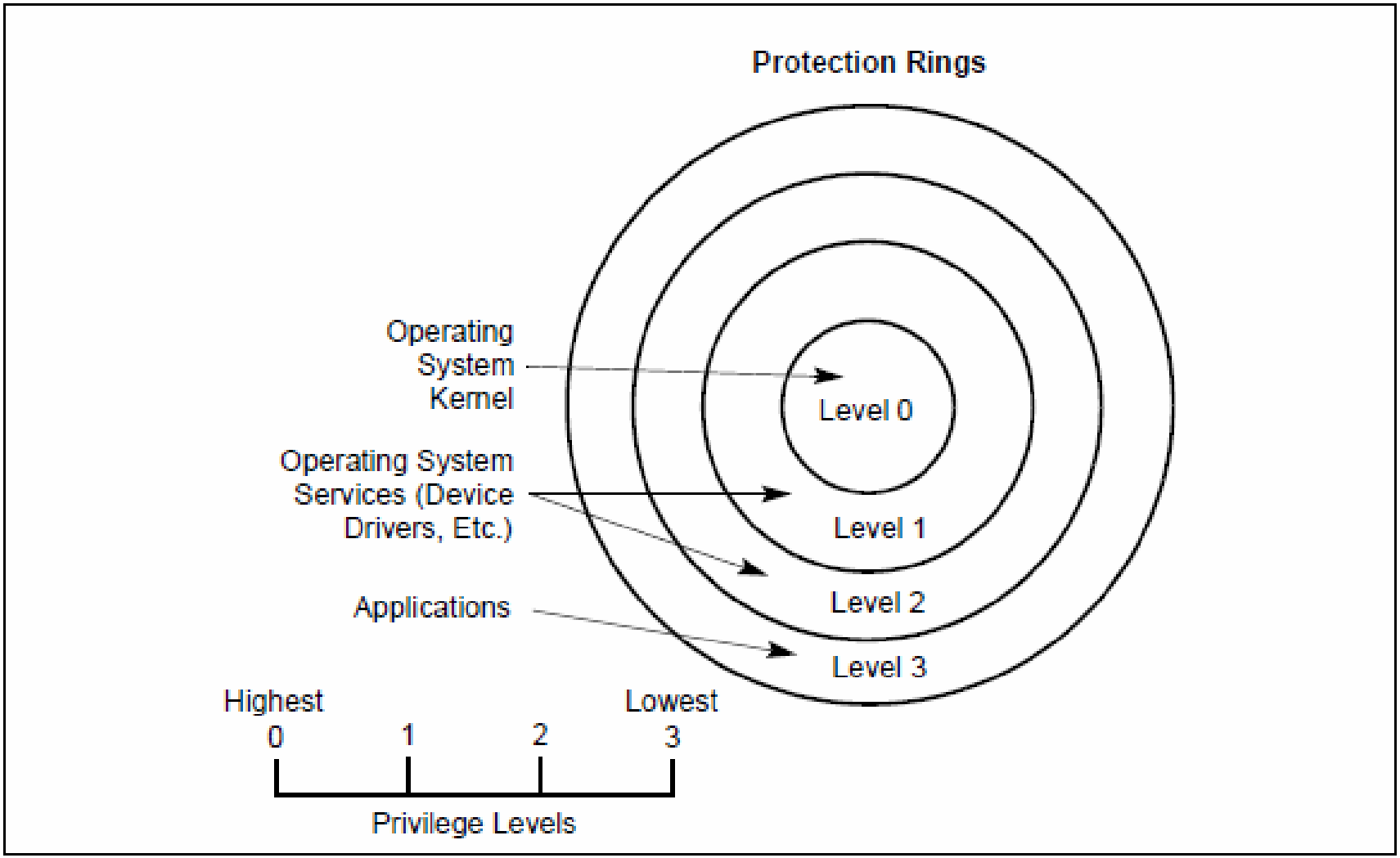

Figure 6-3. Protection Rings

Instruction Set Architecture of IA-32 Task

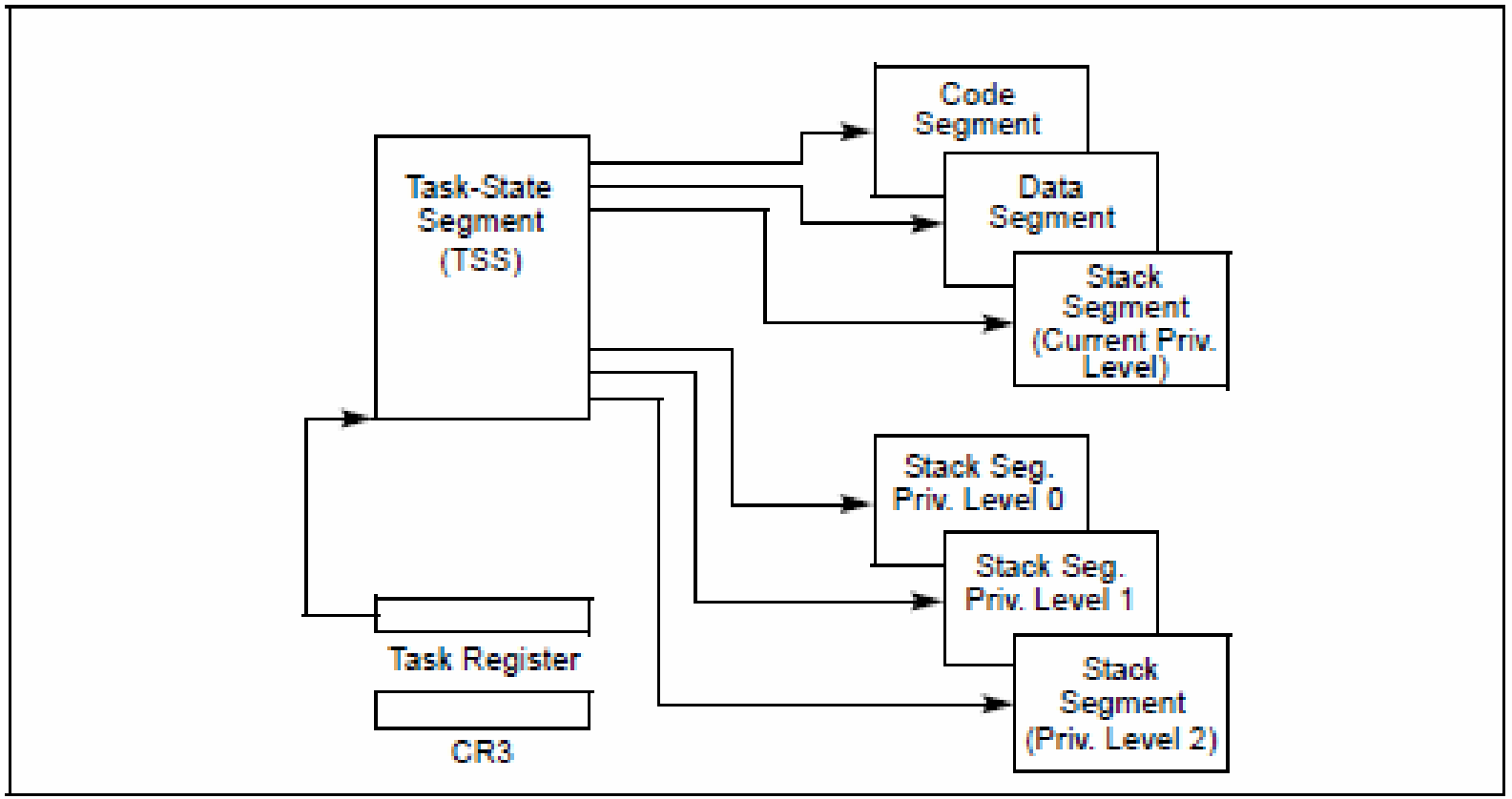

Figure 7-1. Structure of a Task

ISA of IA-32

Task- State Segment

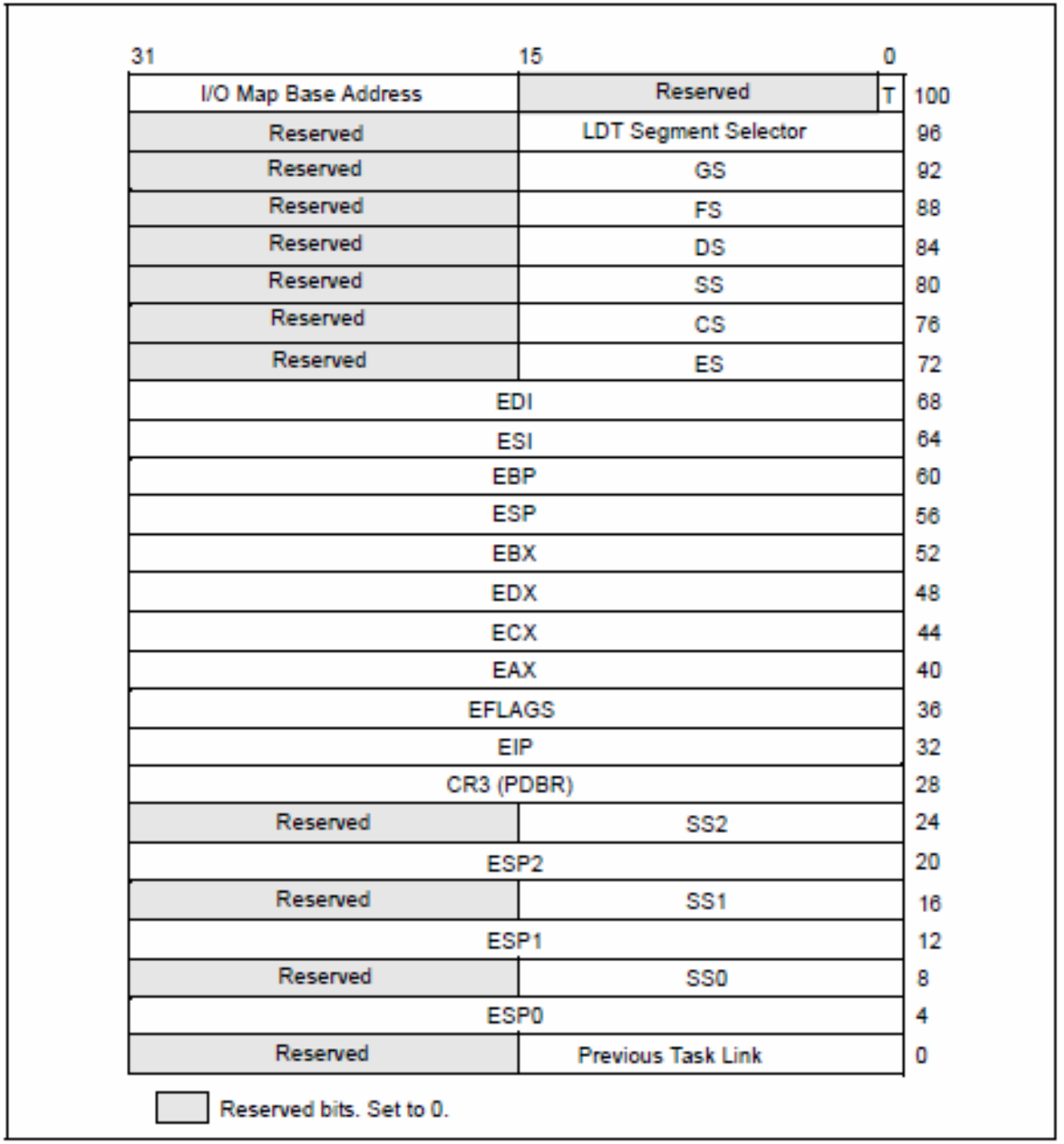

Figure 7-2. 32-Bit Task-State Segment (TSS)

ISA of IA-32

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System-Level Registers and Data Structures devoted to Task Management

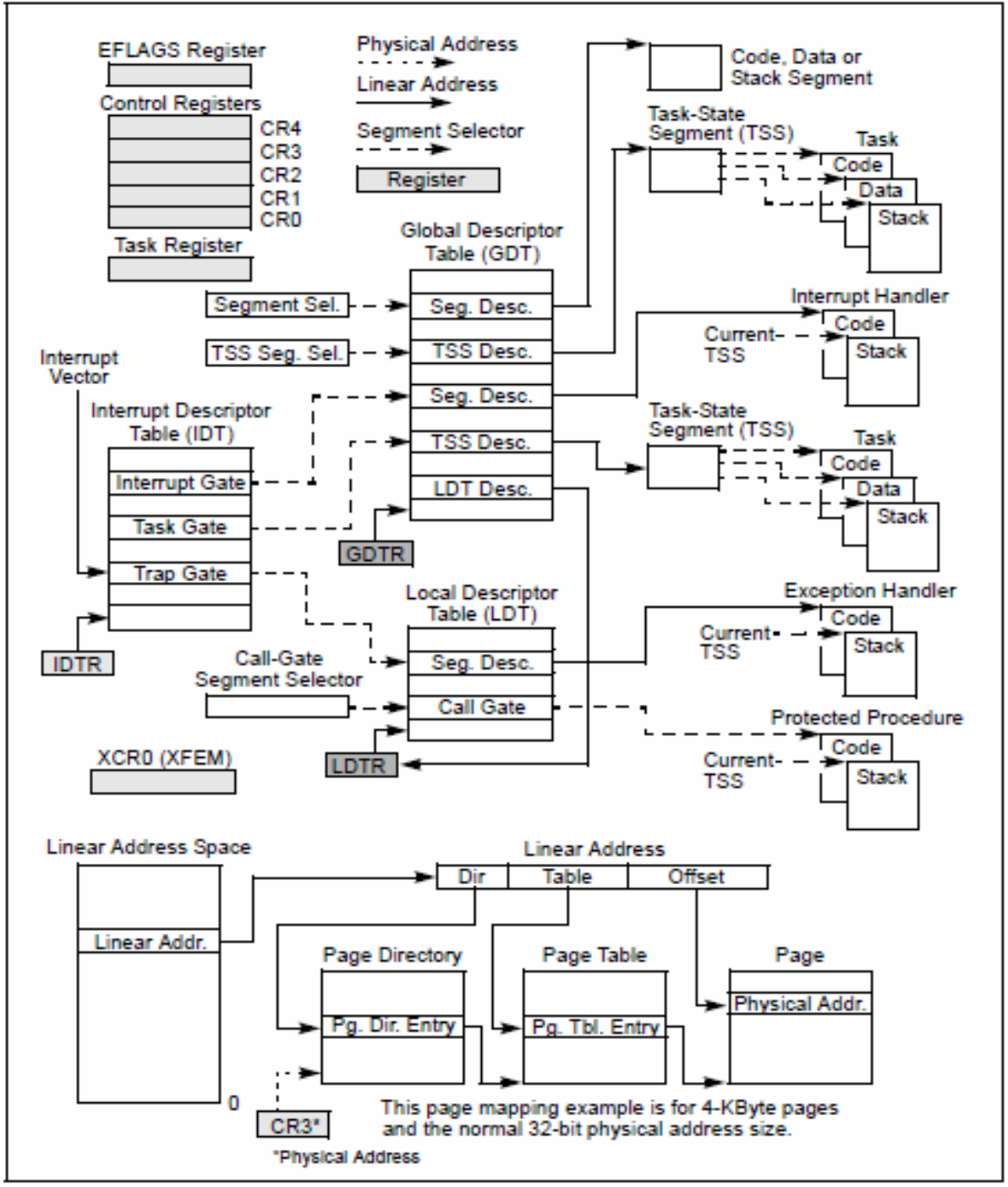

Figure 2-1. IA-32 System-Level Registers and Data Structures

ISA of IA-32

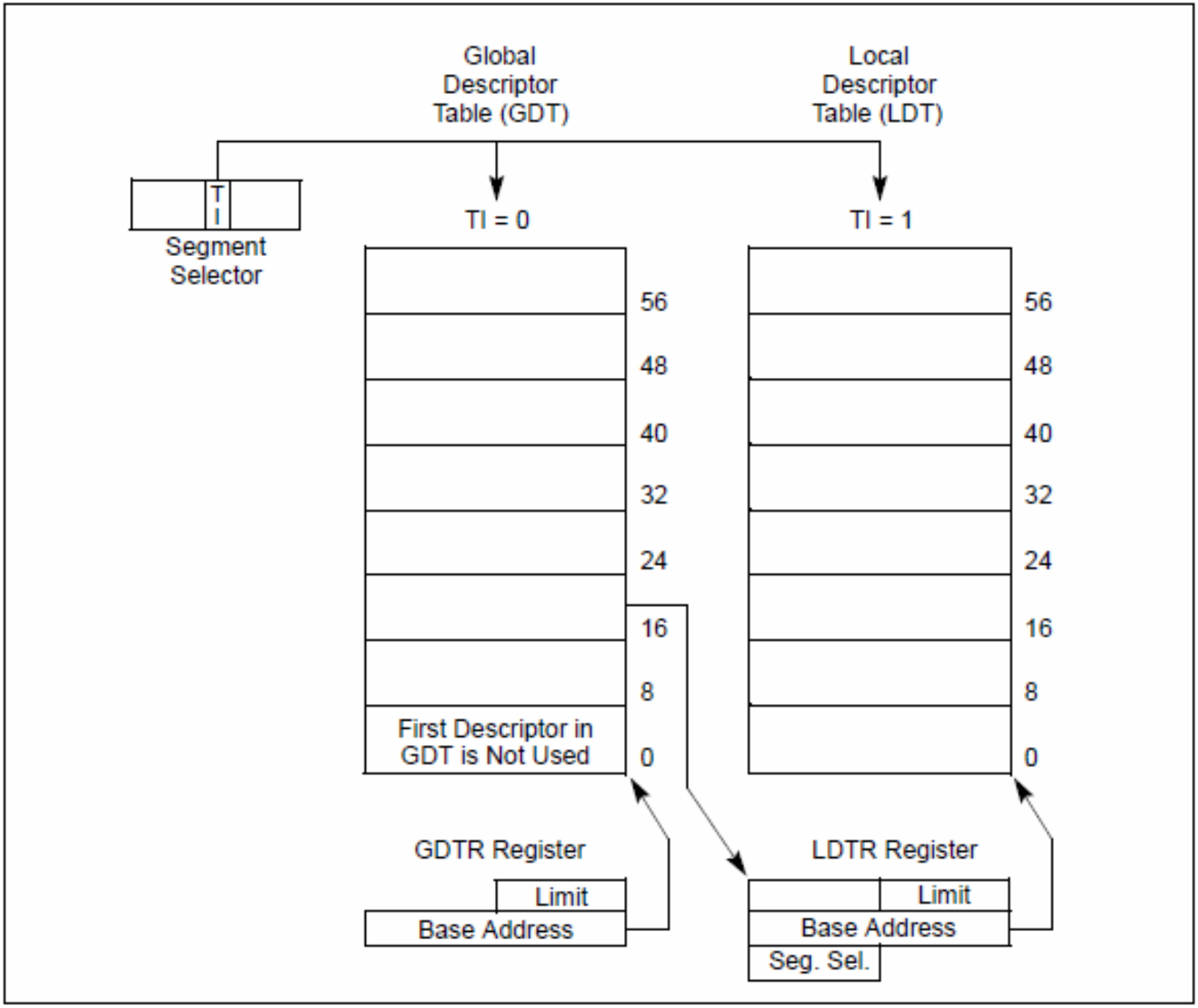

Figure 3-10. Global and Local Descriptor Tables

ISA – Segmentazione e Paginazione in IA-32