

On the Expressive Power of Process Interruption and Compensation

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The investigation of the foundational aspects of linguistic mechanisms for programming long-running transactions (such as the *scope* operator of WS-BPEL) has recently renewed the interest in process algebraic operators that, due to the occurrence of a failure, *interrupt* the execution of one process, replacing it with another one called the *failure handler*. We investigate the decidability of termination problems for two simple fragments of CCS (one with recursion and one with replication) extended with one of two such operators, the *interrupt* operator of CSP and the *try-catch* operator for exception handling. More precisely, we consider the existential termination problem (existence of one terminated computation) and the universal termination problem (all computations terminate). We prove that, as far as the decidability of the considered problems is concerned, under replication there is no difference between interrupt and try-catch (universal termination is decidable while existential termination is not), while under recursion this is not the case (existential termination is undecidable while universal termination is decidable only for interrupt). As a consequence of our undecidability results we show the existence of an expressiveness gap between a fragment of CCS and its extension with either the interrupt or the try-catch operator.

1. Introduction

The investigation of the foundational aspects of the so-called service composition languages, see, e.g., WS-BPEL (OASIS 2003) and WS-CDL (W3C 2004), has recently attracted the attention of the concurrency theory community. In particular, one of the main novelties of such languages is concerned with primitives for programming *long-running transactions*. These primitives permit, on the one hand, to *interrupt* processes when some unexpected failure occurs and, on the other hand, to activate alternative processes named *failure handlers* responsible for compensating those activities that, even if completed, must be undone due to the occurred failure.

Several recent papers propose process calculi that include operators for process failure handling. Just to mention a few, we recall StAC (Butler and Ferreira 2004), cJoin (Bruni *et al.* 2004), cCSP (Butler *et al.* 2003), π_t (Bocchi *et al.* 2003), SAGAS (Bruni *et al.* 2005),

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web-pi (Laneve and Zavattaro 2005), ORC (Misra and Cook 2007), SCC (Boreale *et al.* 2006), COWS (Lapadula *et al.* 2007), SOCK (Guidi *et al.* 2008), and the Conversation Calculus (Vieira *et al.* 2008). This huge amount of calculi, including mechanisms for interruption and failure handling, show evidence of the limitations of usual process calculi including only communication primitives to adequately support a formal investigation of long-running transactions, or of fault and compensation handling in languages for service composition.

In order to perform a formal investigation of these interruption operators we have decided to concentrate on two of them that we consider well established because taken either from the tradition of process calculi or from popular programming languages: the *interrupt* operator of CSP (Hoare 1985) and the *try-catch* operator for exception handling in languages such as C++ or Java. The interrupt operator $P\Delta Q$ executes P until Q executes its first action; when Q starts executing, the process P is definitely interrupted. The try-catch operator $\text{try } P \text{ catch } Q$ executes P , but if P performs a *throw* action it is definitely interrupted and Q is executed instead.

We have found these operators particularly useful because, even if very simple, they are expressive enough to model the typical operators for programming long running transactions. For instance, we can consider an operator $\text{scope}_x(P, F, C)$ corresponding to a simplified version of the *scope* construct of WS-BPEL. The meaning of this operator is as follows. The main activity P is executed. In case a fault is raised by P , its execution is interrupted and the fault handler F is activated. If the main activity P completes, but an outer scope fails and calls for the compensation of the scope x , the compensation handler C is executed.

If we assume that the main activity P communicates internal failure with the action *throw* (we use the typical notation of process calculi: an overlined action, e.g. \bar{a} , is complementary with the corresponding non-overlined one, e.g. action a , and complementary actions allow parallel processes to synchronize) and completion with \overline{end} , and the request for compensation corresponds with the action \bar{x} , we can model the behaviour of $\text{scope}_x(P, F, C)$ with both the try-catch:

$$\text{try } P \text{ catch } F \mid \overline{end.x.C}$$

and the interrupt operator:

$$P\Delta(f.F) \mid \overline{\text{throw}.f} \mid \overline{end.x.C}$$

where the vertical bar means parallel composition.

These two operators are apparently very similar as they both allow for the combination of two processes P and Q , where the first one executes until the second one performs its first action. Nevertheless, there is an interesting distinguishing feature. In the *try-catch* operator, the decision to interrupt the execution of P is taken inside P (by means of the execution of the *throw* action), while in the *interrupt* operator such decision is taken from Q (by executing any initial action). For instance, in the above example of modeling of the $\text{scope}_x(P, F, C)$ operator with the interrupt operator, we had to include an additional process $\overline{\text{throw}.f}$ which captures the request for interruption coming from the main activity and forwards it to the fault handler. Another difference between the

	interrupt	try-catch
	CCS_1^Δ	CCS_1^{tc}
replication	existential termination undecidable universal termination decidable	existential termination undecidable universal termination decidable
	CCS_{rec}^Δ	CCS_{rec}^{tc}
recursion	existential termination undecidable universal termination decidable	existential termination undecidable universal termination undecidable

Table 1. *Summary of the results*

try-catch and the interrupt operators is that the former includes an implicit scoping mechanism which has no counterpart in the interrupt operator. More precisely, the try-catch operator defines a new scope for the special *throw* action which is bound to a specific instance of exception handler.

Starting from these intuitive and informal evaluations of the differences between such operators, we have decided to perform a more rigorous and formal investigation. To this aim, we have considered two restriction-free fragments of CCS (Milner 1989), one with replication and one with restriction, and we have extended them with either the interrupt or the try-catch operator thus obtaining the four different calculi CCS_1^Δ , CCS_1^{tc} , CCS_{rec}^Δ , and CCS_{rec}^{tc} as depicted in Table 1. We have decided to consider calculi without restriction, the standard explicit binder operator of CCS, in order to be able to observe the impact of the implicit binder of try-catch. Moreover, we have decided to consider separately replication and recursion because in CCS there is an interesting interplay between these operators and binders as proved in (Busi *et al.* 2003): in the case of replication it is possible to compute, given a process P , an upper bound to the nesting depth of binders for all derivatives of P (i.e. those processes that can be reached from P after a sequence of transitions). In CCS with recursion, on the contrary, this upper bound cannot be computed in general.

For the obtained four calculi, we have investigated the decidability of the following termination problems: *existential termination* (i.e. there exists a terminated computation) and *universal termination* (i.e. all computations terminate). The obtained results are depicted in Table 1. In order to prove that existential termination is undecidable in the calculi, we reduce the termination problem for Random Access Machines (RAMs) (Shepherdson and Sturgis 1963; Minsky 1967), a well-known Turing complete formalism, to the existential termination problem in CCS_1^Δ and CCS_1^{tc} . As replication is a special case of recursion, we have that the same undecidability result holds also for CCS_{rec}^Δ and CCS_{rec}^{tc} . As far as universal termination is concerned we proceed as follows. We first prove that it is undecidable in CCS_{rec}^{tc} by reduction of the RAM termination problem to universal termination in that calculus. Then we separately prove that universal termination is decidable in CCS_1^{tc} and in CCS_{rec}^Δ . As recursion is more general than replication, the latter result allows us to conclude that universal termination is decidable also in CCS_1^Δ .

The most significant technical contribution of this paper concerns the proof of decidability of universal termination in CCS_{rec}^Δ . This because, while proving decidability

of universal termination in CCS_1^{tc} is done by resorting to the approach in (Busi *et al.* 2003) based on the existence of an upper bound to the nesting depth of operators in the derivatives of a process, proving termination in CCS_{rec}^Δ requires to deal with an unbounded nesting depth of the interrupt operators. For this reason we need to resort to a completely different technique which is based on devising a particular transformation of terms into *trees* (of unbounded depth) and considering an ordering on such trees. The particular transformation devised must be “tuned” in such a way that the ordering obtained is: on the one hand a well-quasi-ordering (and to prove this we exploit the Kruskal Tree theorem (Kruskal 1960)), on the other hand strongly compatible with the operational semantics. Obtaining and proving the latter result is particularly intricate and it also requires us to slightly modify the operational semantics of the interrupt operator in a termination-preserving way and to technically introduce different kinds of trees on subterms and contexts in order to interpret transitions on trees.

Another interesting consequence of our undecidability results (in particular, the undecidability of existential termination) is the existence of an expressiveness gap between a fragment of CCS (without restriction, relabeling, and with guarded choice) and its extension with either interrupt or try-catch. In fact, we observe that for this calculus existential termination is decidable, while this is not the case for its extensions with either interrupt or try-catch. Thus, there exists no computable encoding of the considered interruption operators into this fragment that preserves at least existential termination.

The paper is structured as follows. In Section 2 we define the considered calculi. In Section 3 we prove the undecidability of existential termination in CCS_1^Δ and CCS_1^{tc} . In Section 4 we prove the undecidability of universal termination in CCS_{rec}^{tc} . Section 5 is dedicated to the proof of decidability of universal termination for CCS_1^{tc} and CCS_{rec}^Δ . In Section 6 we evaluate the impact of our results on the evaluation of the expressive power of the considered calculi. Finally, in Section 7 we draw some concluding remarks.

2. The Calculi

We start by considering the fragment of CCS (Milner 1989) without recursion, restriction, and relabeling (that we call finite core CCS or simply finite CCS). Then we present the two infinite extensions with either replication or recursion, the new *interrupt operator*, and finally the *try-catch* operator.

Definition 2.1. (finite core CCS) Let $Name$, ranged over by x, y, \dots , be a denumerable set of channel names. The class of finite core CCS processes is described by the following grammar:

$$P ::= \mathbf{0} \mid \alpha.P \mid P+P \mid P|P \qquad \alpha ::= \tau \mid x \mid \bar{x}$$

The term $\mathbf{0}$ denotes the empty process while the term $\alpha.P$ has the ability to perform the action α (which is either the unobservable τ action or a synchronization on a channel x) and then behaves like P . Two forms of synchronization are available, the output \bar{x} or the input x . The sum construct $+$ is used to make a choice among the summands while

$\text{PRE : } \quad \alpha.P \xrightarrow{\alpha} P$	$\text{PAR : } \quad \frac{P \xrightarrow{\alpha} P'}{P Q \xrightarrow{\alpha} P' Q}$
$\text{SUM : } \quad \frac{P \xrightarrow{\alpha} P'}{P+Q \xrightarrow{\alpha} P'}$	$\text{COM : } \quad \frac{P \xrightarrow{\alpha} P' \quad Q \xrightarrow{\bar{\alpha}} Q'}{P Q \xrightarrow{\tau} P' Q'}$

Table 2. *The transition system for finite core CCS (symmetric rules of PAR and SUM omitted).*

parallel composition $|$ is used to run parallel programs. We denote the process $\alpha.\mathbf{0}$ simply with α .

For input and output actions α , i.e. $\alpha \neq \tau$, we write $\bar{\alpha}$ for the complement of α ; that is, if $\alpha = x$ then $\bar{\alpha} = \bar{x}$, if $\alpha = \bar{x}$ then $\bar{\alpha} = x$. The channel names that occur in P are denoted with $n(P)$. The names in a label α , written $n(\alpha)$, is the set of names in α , i.e. the empty set if $\alpha = \tau$ or the singleton $\{x\}$ if α is either x or \bar{x} .

Table 2 contains the set of the transition rules for finite core CCS.

Definition 2.2. (CCS_!) The class of CCS_! processes is defined by adding the production $P ::= !\alpha.P$ to the grammar of Definition 2.1.

The transition rule for replication is

$$!\alpha.P \xrightarrow{\alpha} P|!\alpha.P$$

Definition 2.3. (CCS_{rec}) We assume a denumerable set of process variables, ranged over by X . The class of CCS_{rec} processes is defined by adding the productions $P ::= X | \text{rec}X.P$ to the grammar of Definition 2.1. In the process $\text{rec}X.P$, $\text{rec}X$ is a binder for the process variable X and P is the scope of the binder. We consider (weakly) guarded recursion, i.e., in the process $\text{rec}X.P$ each occurrence of X (which is free in P) occurs inside a subprocess of the form $\alpha.Q$.

The transition rule for recursion is

$$\frac{P\{\text{rec}X.P/X\} \xrightarrow{\alpha} P'}{\text{rec}X.P \xrightarrow{\alpha} P'}$$

where $P\{\text{rec}X.P/X\}$ denotes the process obtained by substituting $\text{rec}X.P$ for each free occurrence of X in P , i.e. each occurrence of X which is not inside the scope of a binder $\text{rec}X$. Note that CCS_! is equivalent to a fragment of CCS_{rec}. In fact, the replication operator $!\alpha.P$ of CCS_! is equivalent (according, e.g., to the standard definition of strong bisimilarity) to the recursive process $\text{rec}X.(\alpha.(P|X))$.

We now introduce the extensions with the new *process interruption* operator.

Definition 2.4. (CCS_1^Δ and CCS_{rec}^Δ) The class of CCS_1^Δ and CCS_{rec}^Δ processes is defined by adding the production $P ::= P\Delta P$ to the grammars of Definition 2.2 and Definition 2.3, respectively.

The transition rules for the interrupt operator are

$$\frac{P \xrightarrow{\alpha} P'}{P\Delta Q \xrightarrow{\alpha} P'\Delta Q} \quad \frac{Q \xrightarrow{\alpha} Q'}{P\Delta Q \xrightarrow{\alpha} P\Delta Q'}$$

We complete the list of definitions of the considered calculi presenting the extensions with the new *try-catch* operator.

Definition 2.5. (CCS_1^{tc} and $\text{CCS}_{rec}^{\text{tc}}$) The class of CCS_1^{tc} and $\text{CCS}_{rec}^{\text{tc}}$ processes is defined by adding the productions $P ::= \text{try } P \text{ catch } P$ and $\alpha ::= \text{throw}$ to the grammars of Definition 2.2 and Definition 2.3, respectively. The new action **throw** is used to model the raising of an exception.

The transition rules for the try-catch operator are

$$\frac{P \xrightarrow{\alpha} P' \quad \alpha \neq \text{throw}}{\text{try } P \text{ catch } Q \xrightarrow{\alpha} \text{try } P' \text{ catch } Q} \quad \frac{P \xrightarrow{\text{throw}} P'}{\text{try } P \text{ catch } Q \xrightarrow{\tau} Q}$$

We use $\prod_{i \in I} P_i$ to denote the parallel composition of the indexed processes P_i , while we use $\prod_n P$ to denote the parallel composition of n instances of the process P (if $n = 0$ then $\prod_n P$ denotes the empty process $\mathbf{0}$).

In the following we will consider only closed processes, i.e. processes without free occurrences of process variables. Given a closed process Q , its internal runs $Q \longrightarrow Q_1 \longrightarrow Q_2 \longrightarrow \dots$ coincide with sequences of τ labeled transitions, i.e., $P \longrightarrow P'$ iff $P \xrightarrow{\tau} P'$. We denote with \longrightarrow^+ the transitive closure of \longrightarrow , while \longrightarrow^* is the reflexive and transitive closure of \longrightarrow .

A process Q is *dead* if there exists no Q' such that $Q \longrightarrow Q'$. We say that a process P *existentially terminates* if there exists P' s.t. $P \longrightarrow^* P'$ and P' is dead. We say that P *universally terminates* if all its internal runs terminate, i.e. the process P cannot give rise to an infinite computation: formally, P *universally terminates* iff there exists no family $\{P_i\}_{i \in \mathbf{N}}$, s.t. $P_0 = P$ and $P_j \longrightarrow P_{j+1}$ for any j . Observe that *universal termination* implies *existential termination* while the vice versa does not hold.

3. Undecidability of Existential Termination in CCS_1^Δ and CCS_1^{tc}

We prove that CCS_1^Δ and CCS_1^{tc} are powerful enough to model, at least in a non-deterministic way, any Random Access Machine (RAM) (Shepherdson and Sturgis 1963; Minsky 1967), a well-known register-based Turing powerful formalism.

A RAM (denoted in the following with R) is a computational model composed of a finite set of registers r_1, \dots, r_n , that can hold arbitrary large natural numbers, and by

a program composed of indexed instructions $(1 : I_1), \dots, (m : I_m)$, that is a sequence of simple numbered instructions, like arithmetical operations (on the contents of registers) or conditional jumps. An internal state of a RAM is given by (i, c_1, \dots, c_n) where i is the program counter indicating the next instruction to be executed, and c_1, \dots, c_n are the current contents of the registers r_1, \dots, r_n , respectively. Given a configuration (i, c_1, \dots, c_n) , its computation proceeds by executing the instructions in sequence, unless a jump instruction is encountered. The execution stops when an instruction number higher than the length of the program is reached. Note that the computation of the RAM proceeds deterministically (it does not exhibit non-deterministic behaviors).

Without loss of generality, we assume that the registers contain the value 0 at the beginning and at the end of the computation. In other words, the initial configuration is $(1, 0, \dots, 0)$ and, if the RAM terminates, the final configuration is $(i, 0, \dots, 0)$ with $i > m$ (i.e. the instruction I_i is undefined). More formally, we indicate by $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$ the fact that the configuration of the RAM R changes from (i, c_1, \dots, c_n) to (i', c'_1, \dots, c'_n) after the execution of the i -th instruction (\rightarrow_R^* is the reflexive and transitive closure of \rightarrow_R).

The RAM instructions are of two possible formats:

- $(i : Succ(r_j))$: adds 1 to the contents of register r_j ;
- $(i : DecJump(r_j, s))$: if the contents of register r_j is not zero, then decreases it by 1 and goes to the next instruction, otherwise jumps to instruction s .

Our encoding is nondeterministic because it introduces computations which do not follow the expected behavior of the modeled RAM. However, all these computations are infinite. This ensures that, given a RAM, its modeling has a terminating computation if and only if the RAM terminates. This proves that *existential termination* is undecidable.

In this section and in the next one devoted to the proof of the undecidability results, we reason up to a structural congruence \equiv in order to rearrange the order of parallel composed processes and to abstract away from the terminated processes $\mathbf{0}$. We define \equiv as the least congruence relation satisfying the usual axioms $P|Q \equiv Q|P$, $P|(Q|R) \equiv (P|Q)|R$, and $P|\mathbf{0} \equiv P$.

Let R be a RAM with registers r_1, \dots, r_n , and instructions $(1 : I_1), \dots, (m : I_m)$. We model separately registers and instructions.

The program counter is modeled with a message \bar{p}_i indicating that the i -th instruction is the next to be executed. For each $1 \leq i \leq m$, we model the i -th instruction $(i : I_i)$ of R with a process which is guarded by an input operation p_i . Once activated, the instruction performs its operation on the registers and then updates the program counter by producing \bar{p}_{i+1} (or \bar{p}_s in case of jump).

Formally, for any $1 \leq i \leq m$, the instruction $(i : I_i)$ is modeled by $\llbracket (i : I_i) \rrbracket$ which is a shorthand notation for the following processes:

$$\begin{aligned} \llbracket (i : I_i) \rrbracket & : \quad !p_i.(\overline{inc_j}.loop \mid \bar{p}_{i+1}) && \text{if } I_i = Succ(r_j) \\ \llbracket (i : I_i) \rrbracket & : \quad !p_i.(\tau.(loop \mid \overline{dec_j}.loop.loop.\bar{p}_{i+1}) + && \\ & \quad \tau.\overline{zero_j}.ack.\bar{p}_s) && \text{if } I_i = DecJump(r_j, s) \end{aligned}$$

It is worth noting that every time an increment operation is performed, a process \overline{loop}

is spawned. This process will be removed by a corresponding decrement operation. The modeling of the $DecJump(r_j, s)$ instruction internally decides whether to decrement or to test for zero the register.

In case of decrement, if the register is empty the instruction deadlocks because the register cannot be actually decremented. Nevertheless, before trying to decrement the register a process \overline{loop} is generated. As we will discuss in the following, the presence of this process prevents the encoding from terminating. If the decrement operation is actually executed, two instances of process \overline{loop} are removed, one instance corresponding to the one produced before the execution of the decrement, and one instance corresponding to a previous increment operation.

In case of test for zero, the corresponding register will have to be modified as we will discuss below. As this modification on the register requires the execution of several actions, the instruction waits for an acknowledgment before producing the new program counter $\overline{p_s}$.

We now show how to model the registers using either the interrupt or the try-catch operators. In both cases we exploit the following idea. Every time the register r_j is incremented, a dec_j process is spawned which permits the subsequent execution of a corresponding decrement operation. In case of test for zero on the register r_j , we will exploit either the interrupt or the try-catch operators in order to remove all the active processes dec_j , thus resetting the register. If the register is not empty when it is reset, the computation of the encoding does not reproduce the RAM computation any longer. Nevertheless, such “wrong” computation surely does not terminate, thus we can conclude that we faithfully model at least the terminating computations. Divergence in case of “wrong” reset is guaranteed by the fact that if the register is not empty, k instances of dec_j processes are removed with $k > 0$, and k instances of the process \overline{loop} (previously produced by the corresponding k increment operations) will never be removed. As discussed above, the presence of \overline{loop} processes prevents the encoding from terminating. This is guaranteed by considering, e.g., the following divergent process

$$LOOP : loop.(\bar{l} \mid !l.\bar{l})$$

Formally, we model each register r_j , when it contains c_j , with one of the following processes denoted with $\llbracket r_j = c_j \rrbracket^\Delta$ and $\llbracket r_j = c_j \rrbracket^{tc}$:

$$\begin{aligned} \llbracket r_j = c_j \rrbracket^\Delta & : (inc_j.dec_j \mid \prod_{c_j} dec_j) \Delta (zero_j.\overline{nr_j}.ack) \\ \llbracket r_j = c_j \rrbracket^{tc} & : \mathbf{try} (inc_j.dec_j \mid \prod_{c_j} dec_j \mid zero_j.\mathbf{throw}) \mathbf{catch} (\overline{nr_j}.ack) \end{aligned}$$

It is worth observing that, when a test for zero is performed on the register r_j , an output operation $\overline{nr_j}$ is executed before sending the acknowledgment to the corresponding instruction. This action is used to activate a new instance of the process $\llbracket r_j = 0 \rrbracket$, as the process modeling the register r_j is removed by the execution of either the interrupt or the try-catch operators. The activation of new instances of the process modeling the registers is obtained simply considering, for each register r_j , (one of) the two following processes

$$!nr_j.\llbracket r_j = 0 \rrbracket^\Delta \quad !nr_j.\llbracket r_j = 0 \rrbracket^{tc}$$

We are now able to define formally our encoding of RAMs as well as its properties.

Definition 3.1. Let R be a RAM with program instructions $(1 : I_1), \dots, (m : I_m)$ and registers r_1, \dots, r_n . Let also Γ be either Δ or \mathbf{tc} . Given the configuration (i, c_1, \dots, c_n) of R we define

$$\begin{aligned} \llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma = & \\ & \overline{p_i} \mid \llbracket (1 : I_1) \rrbracket \mid \dots \mid \llbracket (m : I_m) \rrbracket \mid \prod_{\sum_{j=1}^n c_j} \overline{\text{loop}} \mid \text{LOOP} \mid \\ & \llbracket r_1 = c_1 \rrbracket^\Gamma \mid \dots \mid \llbracket r_n = c_n \rrbracket^\Gamma \mid !nr_1. \llbracket r_1 = 0 \rrbracket^\Gamma \mid \dots \mid !nr_n. \llbracket r_n = 0 \rrbracket^\Gamma \end{aligned}$$

the encoding of the RAM R in either CCS_1^Δ or $CCS_1^{\mathbf{tc}}$ (taking $\Gamma = \Delta$ or $\Gamma = \mathbf{tc}$, respectively). The processes $\llbracket (i : I_i) \rrbracket$, LOOP , and $\llbracket r_j = c_j \rrbracket^\Gamma$ are as defined above.

The following proposition states that every step of computation of a RAM can be mimicked by the corresponding encoding. On the other hand, the encoding could introduce additional computations. The proposition also states that all these added computations are infinite.

Proposition 3.2. Let R be a RAM with program instructions $(1 : I_1), \dots, (m : I_m)$ and registers r_1, \dots, r_n . Let also Γ be either Δ or \mathbf{tc} . Given a configuration (i, c_1, \dots, c_n) of R , we have that, if $i > m$ and $c_j = 0$ for each j , with $1 \leq j \leq n$, then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ is a dead process, otherwise:

- 1 if $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$ then we have $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma \rightarrow^+ \llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$
- 2 if $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma \rightarrow Q_1 \rightarrow Q_2 \rightarrow \dots \rightarrow Q_l$ is a, possibly zero-length, internal run of $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ then one of the following holds:
 - there exists k , with $1 \leq k \leq l$, such that $Q_k \equiv \llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$, with $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$;
 - $Q_l \rightarrow^+ \llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$, with $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$;
 - Q_l does not existentially terminate.

Proof. First of all, if $i > m$ and $c_i = 0, \forall 1 \leq i \leq n$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ is obviously a dead process because no $\overline{\text{loop}}$ process is included among the parallel processes composing $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ and all other processes are stuck on inputs that cannot be triggered.

If, instead, $i > m$ and there exists i , with $1 \leq i \leq n$, such that $c_i > 0$, then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ can only perform a unique reduction: the one originated by the synchronization with the divergent process LOOP . Thus both statements 1 and 2 are trivially satisfied.

Otherwise, if $i \leq m$, let us suppose $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$. We have two cases:

- If I_i is a $\text{Succ}(r_j)$ instruction the process $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ may perform a reduction sequence composed of two reduction steps that leads to $\llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$: the first reduction is caused by the synchronization on p_i , the second one by the synchronization on inc_j . Every process occurring in such a sequence has at most an alternative reduction: the synchronization with the divergent process LOOP . Thus both statements 1 and 2 are satisfied.

- If I_i is a $DecJump(r_j, s)$ instruction we have two subcases depending on $c_j = 0$ or $c_j > 0$.
 - If $c_j = 0$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ may perform a reduction sequence composed of six or seven reduction steps (six in the case $\Gamma = \mathbf{tc}$, seven in the case $\Gamma = \Delta$) that leads to $\llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$: the first reduction is caused by the synchronization on p_i , the second one by the internal τ action in the righthand side of the choice in the encoding of $DecJump(r_j, s)$, the third one by the synchronization on $zero_j$, then, in the case $\Gamma = \mathbf{tc}$, the fourth one by the execution of *throw* inside the encoding of the r_j register, in the case $\Gamma = \Delta$, the fourth and fifth one by the synchronization on z_j and *stop_j* inside the encoding of the r_j register; finally the last two reductions are caused by the synchronization on nr_j and *ack*. Apart from the process reached by the first reduction step, every process occurring in such a sequence has at most an alternative reduction: the synchronization with the divergent process *LOOP*. Concerning the process reached by the first reduction step we have, possibly, the alternative reduction above plus another alternative reduction: the reduction caused by the internal τ action in the lefthand side of the choice in the encoding of $DecJump(r_j, s)$. Such a reduction leads to a process where an additional instance of the process \overline{loop} is produced and, since synchronization on dec_j cannot occur (because $c_j = 0$), such a process can perform a unique reduction: the one originated by the synchronization with the divergent process *LOOP*. Thus both statements 1 and 2 are satisfied.
 - If $c_j > 0$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma$ may perform a reduction sequence composed of five reduction steps that leads to $\llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$: the first reduction is caused by the synchronization on p_i , the second one by the internal τ action in the lefthand side of the choice in the encoding of $DecJump(r_j, s)$, the third one by the synchronization on dec_j , the fourth and fifth one by the synchronization on *loop* (two times). Apart from the process reached by the first reduction step, every process occurring in such a sequence has at most an alternative reduction: the synchronization with the divergent process *LOOP*. Concerning the process reached by the first reduction step we have, possibly, the alternative reduction above plus another alternative reduction: the reduction caused by the internal τ action in the righthand side of the choice in the encoding of $DecJump(r_j, s)$. Such a reduction leads to a process that may perform a reduction sequence composed of two or three reduction steps (two in the case $\Gamma = \mathbf{tc}$, three in the case $\Gamma = \Delta$) that leads to a process with the following structure of its parallel components: the number of \overline{loop} processes is strictly greater than the sum of the numbers of the dec_k processes for all $1 \leq k \leq n$. Such a sequence is composed by a first reduction caused by the synchronization on $zero_j$ and then: in the case $\Gamma = \mathbf{tc}$, a second reduction caused by the execution of *throw* inside the encoding of the r_j register, in the case $\Gamma = \Delta$, a second and third reduction caused by the synchronization on z_j and *stop_j* inside the encoding of the r_j register. Note that in both cases an interruption (or exception) is generated that, since $c_j > 0$, removes at least an occurrence of a dec_j process (without removing a corresponding number of occurrences of \overline{loop}

processes). It is easy to observe that the process reached by such a sequence does not existentially terminate: the possibility to reach a dead process is crucially conditioned on the capability to remove all \overline{loop} processes (the only thing that can prevent the $LOOP$ divergent process from being activated by synchronization) and the removal of one \overline{loop} process can only be done by the encoding after that one dec_j process is correspondingly removed. Finally, every process occurring in the last reduction sequence has at most an alternative reduction: the synchronization with the divergent process $LOOP$. Thus both statements 1 and 2 are satisfied. \square

Thus, we have the following corollary.

Corollary 3.3. Let R be a RAM. We have that the RAM R terminates if and only if $\llbracket (1, 0, \dots, 0) \rrbracket_R^\Gamma$ existentially terminates (for both $\Gamma = \Delta$ and $\Gamma = \mathbf{tc}$).

Proof. If the RAM R terminates then it is immediate to derive, by induction on the length of the computation of the RAM, from Proposition 3.2 (first part plus statement 1) that $\llbracket (1, 0, \dots, 0) \rrbracket_R^\Gamma$ can reach a dead state.

Concerning the opposite implication, we show that if the RAM R does not terminate then $\llbracket (1, 0, \dots, 0) \rrbracket_R^\Gamma$ does not existentially terminate. By contradiction, if from $\llbracket (1, 0, \dots, 0) \rrbracket_R^\Gamma$ we could reach a dead process this would violate Proposition 3.2 (statement 2). This is shown by induction on the length of an assumed reduction sequence from the encoding of a configuration reached by the non-terminating RAM to the dead process. \square

This proves that existential termination is undecidable in both CCS_1^Δ and $CCS_1^{\mathbf{tc}}$. As replication is a particular case of recursion, we have that the same undecidability result holds also for CCS_{rec}^Δ and $CCS_{rec}^{\mathbf{tc}}$.

4. Undecidability of Universal Termination in $CCS_{rec}^{\mathbf{tc}}$

In this section we prove that also universal termination is undecidable in $CCS_{rec}^{\mathbf{tc}}$. This result follows from the existence of a deterministic encoding of RAMs satisfying the following stronger soundness property: a RAM terminates if and only if the corresponding encoding universally terminate.

The basic idea of the new modeling is to represent the number c_j , stored in the register r_j , with a process composed of c_j nested try-catch operators. This approach can be adopted in $CCS_{rec}^{\mathbf{tc}}$ because standard recursion admits recursion in *depth*, while it was not applicable in $CCS_1^{\mathbf{tc}}$ because replication supports only recursion in *width*. By recursion in width we mean that the recursively defined term can expand only in parallel as, for instance, in $recX.(P|X)$ (corresponding to the replicated process $!P$) where the variable X is an operand of the parallel composition operator. By recursion in depth, we mean that the recursively defined term expands also under other operators such as, for instance, in $recX.(\mathbf{try}(P|X)\mathbf{catch}Q)$ (corresponding to an unbounded nesting of try-catch operators).

Let R be a RAM with registers r_1, \dots, r_n , and instructions $(1 : I_1), \dots, (m : I_m)$. We start presenting the modeling of the instructions which is similar to the encoding presented in the previous section. Note that here the assumption on registers to all have value 0 in a terminating configuration is not needed. We encode each instruction $(i : I_i)$ with the process $\llbracket (i : I_i) \rrbracket$, which is a shorthand for the following process

$$\begin{aligned} \llbracket (i : I_i) \rrbracket & : \text{rec}X.p_i.(\overline{\text{inc}_j.p_{i+1}}|X) && \text{if } I_i = \text{Succ}(r_j) \\ \llbracket (i : I_i) \rrbracket & : \text{rec}X.p_i.(\overline{\text{zero}_j.p_s} + \overline{\text{dec}_j.ack.p_{i+1}}|X) && \text{if } I_i = \text{DecJump}(r_j, s) \end{aligned}$$

As in the previous section, the program counter is modeled by the process $\overline{p_i}$ which indicates that the next instruction to execute is $(i : I_i)$. The process $\llbracket (i : I_i) \rrbracket$ simply consumes the program counter process, then updates the registers (resp. performs a test for zero), and finally produces the new program counter process $\overline{p_{i+1}}$ (resp. $\overline{p_s}$). Notice that in the case of a decrement operation, the instruction process waits for an acknowledgment before producing the new program counter process. This is necessary because the register decrement requires the execution of several operations.

The register r_j , that we assume initially empty, is modeled by the process $\llbracket r_j = 0 \rrbracket$ which is a shorthand for the following process (to simplify the notation we use also the shorthand R_j defined below)

$$\begin{aligned} \llbracket r_j = 0 \rrbracket & : \text{rec}X.(\text{zero}_j.X + \text{inc}_j.\text{try } R_j \text{ catch } (\overline{\text{ack}.X})) \\ R_j & : \text{rec}Y.(\text{dec}_j.\text{throw} + \text{inc}_j.\text{try } Y \text{ catch } (\overline{\text{ack}.Y})) \end{aligned}$$

The process $\llbracket r_j = 0 \rrbracket$ is able to react either to test for zero requests or increment operations. In the case of increment requests, a try-catch operator is activated. Inside this operator a recursive process is installed which reacts to either increment or decrement requests. In the case of an increment, an additional try-catch operator is activated (thus increasing the number of nested try-catch). In the case of a decrement, a failure is raised which removes the active try-catch operator (thus decreasing the number of nested try-catch) and emits the acknowledgment required by the instruction process. When the register returns to be empty, the outer recursion reactivates the initial behavior.

Formally, we have that the register r_j with contents $c_j > 0$ is modeled by the following process composed of the nesting of c_j try-catch operators

$$\begin{aligned} \llbracket r_j = c_j \rrbracket & : \text{try} \\ & \quad (\text{try} \\ & \quad \quad (\dots \\ & \quad \quad \quad \text{try } R_j \text{ catch } (\overline{\text{ack}.R_j}) \\ & \quad \quad \quad \dots) \\ & \quad \quad \text{catch } (\overline{\text{ack}.R_j})) \\ & \quad \text{catch } (\overline{\text{ack}. \llbracket r_j = 0 \rrbracket}) \end{aligned}$$

where R_j is as defined above. We are now able to define formally the encoding of RAMs in CCS_{rec}^{tc} .

Definition 4.1. Let R be a RAM with program instructions $(1 : I_1), \dots, (m : I_m)$ and

registers r_1, \dots, r_n . Given the configuration (i, c_1, \dots, c_n) we define with

$$\llbracket (i, c_1, \dots, c_n) \rrbracket_R = \overline{p_i} \mid \llbracket (1 : I_1) \rrbracket \mid \dots \mid \llbracket (m : I_m) \rrbracket \mid \llbracket r_1 = c_1 \rrbracket \mid \dots \mid \llbracket r_n = c_n \rrbracket$$

the encoding of the RAM R in CCS_{rec}^{tc} .

The new encoding faithfully reproduces the behavior of a RAM as stated by the following proposition. In the following Proposition we use the notion of *deterministic* internal run defined as follows: an internal run $P_0 \longrightarrow P_1 \longrightarrow \dots \longrightarrow P_l$ is deterministic if for every process P_i , with $i < l$, P_{i+1} is the unique process Q such that $P_i \longrightarrow Q$.

Proposition 4.2. Let R be a RAM with program instructions $(1 : I_1), \dots, (m : I_m)$ and registers r_1, \dots, r_n . Given a configuration (i, c_1, \dots, c_n) of R , we have that, if $i > m$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R$ is a dead process, otherwise:

- 1 if $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$ then we have $\llbracket (i, c_1, \dots, c_n) \rrbracket_R \rightarrow^+ \llbracket (i', c'_1, \dots, c'_n) \rrbracket_R$
- 2 there exists a non-zero length deterministic internal run $\llbracket (i, c_1, \dots, c_n) \rrbracket_R^\Gamma \longrightarrow Q_1 \longrightarrow Q_2 \longrightarrow \dots \longrightarrow \llbracket (i', c'_1, \dots, c'_n) \rrbracket_R^\Gamma$ such that $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$.

Proof. First of all, if $i > m$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R$ is obviously a dead process because all processes (that compose it by means of parallel) are stuck on inputs that cannot be triggered.

Otherwise, if $i \leq m$, let us suppose $(i, c_1, \dots, c_n) \rightarrow_R (i', c'_1, \dots, c'_n)$. We have two cases:

- If I_i is a *Succ*(r_j) instruction the process $\llbracket (i, c_1, \dots, c_n) \rrbracket_R$ proceeds deterministically by performing a reduction sequence composed of two reduction steps that leads to $\llbracket (i', c'_1, \dots, c'_n) \rrbracket_R$: the first reduction is caused by the synchronization on p_i , the second one by the synchronization on *inc* _{j} . Thus both statements 1 and 2 are satisfied.
- If I_i is a *DecJump*(r_j, s) instruction we have two subcases depending on $c_j = 0$ or $c_j > 0$.
 - If $c_j = 0$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R$ proceeds deterministically by performing a reduction sequence composed of two reduction steps that leads to $\llbracket (i', c'_1, \dots, c'_n) \rrbracket_R$: the first reduction is caused by the synchronization on p_i , the second one by the synchronization on *zero* _{j} . Thus both statements 1 and 2 are satisfied.
 - If $c_j > 0$ then $\llbracket (i, c_1, \dots, c_n) \rrbracket_R$ proceeds deterministically by performing a reduction sequence composed of four reduction steps that leads to $\llbracket (i', c'_1, \dots, c'_n) \rrbracket_R$: the first reduction is caused by the synchronization on p_i , the second one by the synchronization on *dec* _{j} , the third one by the execution of *throw* inside the innermost *try – catch* clause in the encoding of the r_j register and the fourth one by the synchronization on *ack*. Thus both statements 1 and 2 are satisfied.

□

Thus, we have the following corollary.

Corollary 4.3. Let R be a RAM. We have that the RAM R terminates if and only if $\llbracket (1, 0, \dots, 0) \rrbracket_R$ universally terminates.

Proof. The proof of this corollary is the same as that of Corollary 4.3 referring to Proposition 4.2 instead of Proposition 3.2. \square

This proves that universal termination is undecidable in CCS_{rec}^{tc} .

5. Decidability of Universal Termination in CCS_{rec}^{tc} and CCS_{rec}^{Δ}

In the RAM encoding presented in the previous section natural numbers are represented by chains of nested try-catch operators, that are constructed by exploiting recursion. In this section we prove that both recursion and try-catch are strictly necessary. In fact, if we consider replication instead of recursion or the interrupt operator instead of the try-catch operator, universal termination turns out to be decidable.

These results are based on the theory of well-structured transition systems (Finkel and Schnoebelen 2001). We start recalling some basic definitions and results concerning well-structured transition systems, that will be used in the following.

A *quasi-ordering*, also known as pre-order, is a reflexive and transitive relation.

Definition 5.1. A *well-quasi-ordering* (wqo) is a quasi-ordering \leq over a set \mathcal{S} such that, for any infinite sequence s_0, s_1, s_2, \dots in \mathcal{S} , there exist indexes $i < j$ such that $s_i \leq s_j$.

Transition systems can be formally defined as follows.

Definition 5.2. A *transition system* is a structure $TS = (\mathcal{S}, \rightarrow)$, where \mathcal{S} is a set of *states* and $\rightarrow \subseteq \mathcal{S} \times \mathcal{S}$ is a set of *transitions*. We write $Succ(s)$ to denote the set $\{s' \in \mathcal{S} \mid s \rightarrow s'\}$ of immediate successors of S . TS is *finitely branching* if all $Succ(s)$ are finite.

Well-structured transition systems, defined as follows, provide the key tool to decide properties of computations.

Definition 5.3. A *well-structured transition system with strong compatibility* is a transition system $TS = (\mathcal{S}, \rightarrow)$, equipped with a quasi-ordering \leq on \mathcal{S} , such that the two following conditions hold:

- 1 **well-quasi-ordering:** \leq is a well-quasi-ordering, and
- 2 **strong compatibility:** \leq is (upward) compatible with \rightarrow , i.e., for all $s_1 \leq t_1$ and all transitions $s_1 \rightarrow s_2$, there exists a state t_2 such that $t_1 \rightarrow t_2$ and $s_2 \leq t_2$.

In the following we use the notation $(\mathcal{S}, \rightarrow, \leq)$ for transition systems equipped with a quasi-ordering \leq .

The following theorem (a special case of a result in (Finkel and Schnoebelen 2001)) will be used to obtain our decidability results.

Theorem 5.4. Let $(\mathcal{S}, \rightarrow, \leq)$ be a finitely branching, well-structured transition system with strong compatibility, decidable \leq and computable $Succ$. The existence of an infinite computation starting from a state $s \in \mathcal{S}$ is decidable.

The proof of decidability of universal termination in CCS_{rec}^{Δ} is not done on the original transition system, but on a termination equivalent one. The new transition system does

not eliminate interrupt operators during the computation; in this way, the nesting of interrupt operators can only grow and does not shrink. As we will see, this transformation will be needed for proving that the ordering that we consider on processes is strongly compatible with the operational semantics. Formally, we define the new transition system $\xrightarrow{\alpha}$ for CCS_{rec}^{Δ} considering the transition rules of Definition 2.3 (where $\xrightarrow{\alpha}$ is substituted for $\xrightarrow{\alpha}$) plus the following rules

$$\frac{P \xrightarrow{\alpha} P'}{P \Delta Q \xrightarrow{\alpha} P' \Delta Q} \quad \frac{Q \xrightarrow{\alpha} Q'}{P \Delta Q \xrightarrow{\alpha} Q' \Delta \mathbf{0}}$$

Notice that the first of the above rules is as in Definition 2.4, while the second one is different because it does not remove the Δ operator.

As done for the standard transition system, we assume that the reductions $\xrightarrow{\alpha}$ of the new semantics corresponds to the τ -labeled transitions $\xrightarrow{\tau}$. Also for the new semantics, we say that a process P universally terminates if and only if all its computations are finite, i.e. it cannot give rise to an infinite sequence of reductions $\xrightarrow{\alpha}$.

To prove the equivalence of the semantics of CCS_{rec}^{Δ} presented in Section 2 with the alternative semantics presented in this section with respect to termination, we need to define the following congruence between processes:

Definition 5.5. We define \equiv_T as the least congruence relation satisfying the following axiom:

$$P \Delta \mathbf{0} \equiv_T P$$

The equivalence result (equivalence with respect to termination) can be easily proved with the help of the following propositions:

Proposition 5.6. Let $P, Q \in CCS_{rec}^{\Delta}$ with $P \equiv_T Q$. If $P \xrightarrow{\alpha} P'$ then there exists Q' such that $Q \xrightarrow{\alpha} Q'$ and $P' \equiv_T Q'$.

Proof. By induction on the proof of the relation $P \equiv_T Q$. □

Proposition 5.7. Let $P, Q \in CCS_{rec}^{\Delta}$ with $P \equiv_T Q$. If $P \xrightarrow{\alpha} P'$ then there exists Q' such that $Q \xrightarrow{\alpha} Q'$ and $P' \equiv_T Q'$.

Proof. By induction on the proof of the relation $P \equiv_T Q$. □

Proposition 5.8. Let $P \in CCS_{rec}^{\Delta}$. If $P \xrightarrow{\alpha} P'$ then there exists P'' such that $P \xrightarrow{\alpha} P''$ and $P' \equiv_T P''$.

Proof. By induction on the proof of the derivation $P \xrightarrow{\alpha} P'$. □

Proposition 5.9. Let $P \in CCS_{rec}^{\Delta}$. If $P \xrightarrow{\alpha} P'$ then there exists P'' such that $P \xrightarrow{\alpha} P''$ and $P' \equiv_T P''$.

Proof. By induction on the proof of the derivation $P \xrightarrow{\alpha} P'$. □

Corollary 5.10. Let $P \in CCS_{rec}^\Delta$. Then P universally terminates according to the semantics \longrightarrow iff P universally terminates according to the new semantics \mapsto .

We now separate in two subsections the proofs of decidability of universal termination in $CCS_{rec}^{\dagger c}$ and in CCS_{rec}^Δ .

5.1. Universal termination is decidable in $(CCS_{rec}^{\dagger c}, \longrightarrow)$

The proof for $CCS_{rec}^{\dagger c}$ is just a reformulation of the proof of decidability of universal termination in CCS without relabeling and with replication instead of recursion reported in (Busi *et al.* 2008).

We define for $(CCS_{rec}^{\dagger c}, \longrightarrow)$ a quasi-ordering on processes which turns out to be a well-quasi-ordering compatible with \longrightarrow . Thus, exploiting Theorem 5.4 we show that universal termination is decidable.

Definition 5.11. Let $P \in CCS_{rec}^{\dagger c}$. With $Deriv(P)$ we denote the set of processes reachable from P with a sequence of reduction steps:

$$Deriv(P) = \{Q \mid P \longrightarrow^* Q\}$$

To define the wqo on processes we need the following structural congruence.

Definition 5.12. We define \equiv as the least congruence relation satisfying the following axioms: $P|Q \equiv Q|P$ $P|(Q|R) \equiv (P|Q)|R$ $P|\mathbf{0} \equiv P$

Now we are ready to define the quasi-ordering on processes:

Definition 5.13. Let $P, Q \in CCS_{rec}^{\dagger c}$. We write $P \preceq Q$ iff there exist $n, P', R, P_1, \dots, P_n, Q_1, \dots, Q_n, S_1, \dots, S_n$ such that $P \equiv P' | \prod_{i=1}^n \text{try } P_i \text{ catch } S_i$, $Q \equiv P' | R | \prod_{i=1}^n \text{try } Q_i \text{ catch } S_i$, and $P_i \preceq Q_i$ for $i = 1, \dots, n$.

The above definition can be seen as a definition by induction on the nesting depth of try-catch operators. In the base case, we have $n = 0$, thus $P \preceq Q$ if and only if Q contains the processes in P plus other processes in parallel. In the inductive case, $P \preceq Q$ if each process $\text{try } P_i \text{ catch } S_i$ occurring in P has a corresponding process $\text{try } Q_i \text{ catch } S_i$ with $P_i \preceq Q_i$.

Theorem 5.14. Let $P \in CCS_{rec}^{\dagger c}$. Then the transition system $(Deriv(P), \longrightarrow, \preceq)$ is a finitely branching well-structured transition system with strong compatibility, decidable \preceq and computable *Succ*.

Proof. In the following we show how to precisely obtain the proof of this result from the analogous proof of decidability of universal termination in CCS without relabeling and with replication instead of recursion reported in (Busi *et al.* 2008).

First we need to introduce some auxiliary definitions. Let $P \in CCS_{rec}^{\dagger c}$. With $d_{tc}(P)$

we denote the maximum number of nested try-catch operators in process P :

$$\begin{aligned} d_{\text{tc}}(\mathbf{0}) &= 0 \\ d_{\text{tc}}(\alpha.P) &= d_{\text{tc}}(!\alpha.P) = d_{\text{tc}}(P) \\ d_{\text{tc}}(P + Q) &= d_{\text{tc}}(P|Q) = \max(\{d_{\text{tc}}(P), d_{\text{tc}}(Q)\}) \\ d_{\text{tc}}(\text{try } P \text{ catch } Q) &= \max(\{1 + d_{\text{tc}}(P), d_{\text{tc}}(Q)\}) \end{aligned}$$

The set of sequential subprocesses of P is defined as:

$$\begin{aligned} \text{Sub}(\mathbf{0}) &= \{\mathbf{0}\} \\ \text{Sub}(\alpha.P) &= \{\alpha.P\} \cup \text{Sub}(P) \\ \text{Sub}(!\alpha.P) &= \{!\alpha.P\} \cup \text{Sub}(P) \\ \text{Sub}(P + Q) &= \{P + Q\} \cup \text{Sub}(P) \cup \text{Sub}(Q) \\ \text{Sub}(P|Q) &= \text{Sub}(P) \cup \text{Sub}(Q) \\ \text{Sub}(\text{try } P \text{ catch } Q) &= \text{Sub}(P) \cup \text{Sub}(Q) \end{aligned}$$

Finally, let $\text{catch}(P)$ be the set of the processes used as handlers of exceptions in *try* – *catch* operators occurring in P :

$$\text{catch}(P) = \{S \mid \exists Q : \text{try } Q \text{ catch } S \text{ occurs in } P\}$$

The proof of the theorem is then performed by using exactly the same formal machinery as that used in (Busi *et al.* 2008) for proving the corresponding Theorem 8 of (Busi *et al.* 2008), that includes Definition 17, Lemma 2, Propositions 10, 11, 12, 13, Corollary 2 and Theorems 6, 7 of (Busi *et al.* 2008), with the following replacements. The formal machinery of (Busi *et al.* 2008) listed above (statements plus proofs) must be considered where: all occurrences of CCS_i in (Busi *et al.* 2008) are replaced by CCS_i^{tc} , \equiv_w by \equiv , $bn(P)$ by $\text{catch}(P)$, d_ν by d_{tc} , \mapsto by \longrightarrow , $(\nu x_i)Q$ by $\text{try } Q \text{ catch } S_i$, x_i by S_i and, finally, in the proof of Theorem 8 of (Busi *et al.* 2008) we must replace the justification for the statement that the transition system of P is finitely branching with the following one. The fact that $(\text{Deriv}(P), \longrightarrow)$ is finitely branching derives from an inspection of the transition rules (in particular of the operational rule for the $!\alpha.P$ operator presented in Section 2 of this paper). \square

Corollary 5.15. Let $P \in CCS_i^{\text{tc}}$. The universal termination of process P is decidable.

5.2. Universal termination is decidable in $(CCS_{rec}^\Delta, \mapsto)$

According to the ordering defined in Definition 5.13, we have that $P \preceq Q$ if Q has the same structure of nesting of try-catch operators and it is such that in each point of this nesting Q contains at least the same processes (plus some other processes in parallel). This is a well-quasi-ordering in the calculus with replication because, given P , it is possible to compute an upper bound to the number of nestings in any process in $\text{Deriv}(P)$. In the calculus with recursion this upper bound does not exist as recursion permits to generate nesting of unbounded depth (this e.g. is used in the deterministic RAM modeling of Section 4). For this reason, we need to move to a different ordering inspired by the ordering on trees used by Kruskal in (Kruskal 1960). This allows us to

use the Kruskal Tree theorem that states that the trees defined on a well-quasi-ordering form a well-quasi-ordering.

The remainder of this section is devoted to the definition of how to associate trees to processes of CCS_{rec}^Δ , and how to extract from these trees an ordering for $(CCS_{rec}^\Delta, \mapsto)$ which turns out to be a wqo.

We take \mathcal{E} to be the set of (open) terms of CCS_{rec}^Δ and \mathcal{P} to be the set of CCS_{rec}^Δ processes, i.e. closed terms. \mathcal{P}_{seq} is the subset of \mathcal{P} of terms P such that either $P = \mathbf{0}$ or $P = \alpha.P_1$ or $P = P_1 + P_2$ or $P = recX.P_1$, with $P_1, P_2 \in \mathcal{E}$. Let $\mathcal{P}_{int} = \{P\Delta Q \mid P, Q \in \mathcal{P}\}$.

Given a set E , we denote with E^* the set of finite sequences of elements in E . We use “;” as a separator for elements of a set E when denoting a sequence $w \in E^*$, ϵ to denote the empty sequence and $len(w)$ to denote the length of a sequence w . Finally, we use w_i to denote the i -th element in the sequence w (starting from 1) and $e \in w$ to stand for $e \in \{w_i \mid 1 \leq i \leq len(w)\}$.

Definition 5.16. Let $P \in \mathcal{P}$. We define the flattened parallel components of P , $FPAR(P)$, as the sequence over $\mathcal{P}_{seq} \cup \mathcal{P}_{int}$ given by

$$\begin{aligned} FPAR(P_1|P_2) &= FPAR(P_1); FPAR(P_2) \\ FPAR(P) &= P \text{ if } P \in \mathcal{P}_{seq} \cup \mathcal{P}_{int} \end{aligned}$$

Given a sequence $w \in E^*$ we define the sequence $w' \in E'^*$ obtained by filtering w with respect to $E' \subseteq E$ as follows. For $1 \leq i \leq len(w)$, $w'_i = w_{k_i}$, where $k \in \{1, \dots, len(w)\}^*$ is such that k is strictly increasing, i.e. $j' > j$ implies $k_{j'} > k_j$, and, for all h , $w_h \in E'$ if and only if $h \in k$. In the following we call $FINT(P)$ the sequence obtained by filtering $FPAR(P)$ with respect to \mathcal{P}_{int} and $FSEQ(P)$ the sequence obtained by filtering $FPAR(P)$ with respect to \mathcal{P}_{seq} .

In the following we map processes into ordered trees (with both a left to right ordering of children at every node and the usual child to parent ordering). We use \mathbf{N} to denote the set of positive natural numbers, i.e. $\mathbf{N} = \{1, 2, \dots\}$.

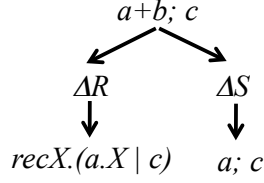
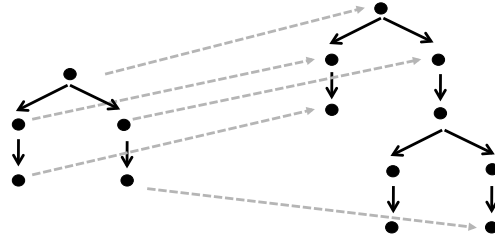
Definition 5.17. A tree t over a set E is a partial function from \mathbf{N}^* to E such that $dom(t)$ is finite, is closed with respect to sequence prefixing and is such that $\vec{n}; m \in dom(t)$ and $m' \leq m$, with $m' \in \mathbf{N}$, implies $\vec{n}; m' \in dom(t)$.

Example 5.18. $(\epsilon, l) \in t$ denotes that the root of the tree has label $l \in E$; $(1; 2, l) \in t$ denotes that the second son of the first son of the root of the tree t has label $l \in E$.

Let $\mathcal{P}_{inth} = \{\Delta Q \mid Q \in \mathcal{P}\}$ be a set representing interruption handlers.

Definition 5.19. Let $P \in \mathcal{P}$. We define the tree of P , $TREE(P)$, as the minimal tree $TREE(P)$ over $\mathcal{P}_{seq}^* \cup \mathcal{P}_{inth}$ (and minimal auxiliary tree $TREE^{odd}(P')$ over $\mathcal{P}_{seq}^* \cup \mathcal{P}_{inth}$, with $P' \in \mathcal{P}_{int}$) satisfying

$$\begin{aligned} (\epsilon, FSEQ(P)) &\in TREE(P) \\ (\vec{n}, l) \in TREE^{odd}(FINT(P)_i) &\text{ implies } (i; \vec{n}, l) \in TREE(P) \end{aligned}$$


 Fig. 1. Tree t associated to process P of Example 5.20.

 Fig. 2. Function φ from $dom(t)$ to $dom(t')$ of Example 5.22 strictly preserves order inside trees.

$$\begin{aligned}
 (\epsilon, \Delta Q) &\in TREE^{odd}(P' \Delta Q) \\
 (\vec{n}, l) \in TREE(P') &\text{ implies } (1; \vec{n}, l) \in TREE^{odd}(P' \Delta Q)
 \end{aligned}$$

Example 5.20. The tree t of the process $P = (a + b) | ((recX.(a.X | c)) \Delta R) | c | ((a | c) \Delta S)$ for some processes R and S is $t = \{(\epsilon, a + b; c), (1, \Delta R), (1; 1, recX.(a.X | c)), (2, \Delta S), (2; 1, a; c)\}$. The tree t is depicted in Fig. 1.

In the following, we define the ordering between processes by resorting to the ordering on trees used in (Kruskal 1960) applied to the particular trees obtained from processes by our transformation procedure. In particular, in order to do this we introduce the notion of injective function that strictly preserves order inside trees: a possible formal way to express homeomorphic embedding between trees, used in Kruskal's theorem (Kruskal 1960), that we take from (Simpson 1985).

We take \leq_T to be the ancestor pre-order relation inside trees, defined by: $\vec{n} \leq_T \vec{m}$ iff \vec{m} is a prefix of \vec{n} (or $\vec{m} = \vec{n}$). Moreover, we take \wedge_T to be the minimal common ancestor of a pair of nodes, i.e. $\vec{n}_1 \wedge_T \vec{n}_2 = \min\{\vec{m} | \vec{n}_1 \leq_T \vec{m} \wedge \vec{n}_2 \leq_T \vec{m}\}$.

Definition 5.21. We say that an injective function φ from $dom(t)$ to $dom(t')$ strictly preserves order inside trees iff for every $\vec{n}, \vec{m} \in dom(t)$ we have:

- $\vec{n} \leq_T \vec{m}$ implies $\varphi(\vec{n}) \leq_T \varphi(\vec{m})$
- $\varphi(\vec{n} \wedge_T \vec{m}) = \varphi(\vec{n}) \wedge_T \varphi(\vec{m})$

Example 5.22. Consider tree t of Example 5.20 and a tree t' such that $dom(t') =$

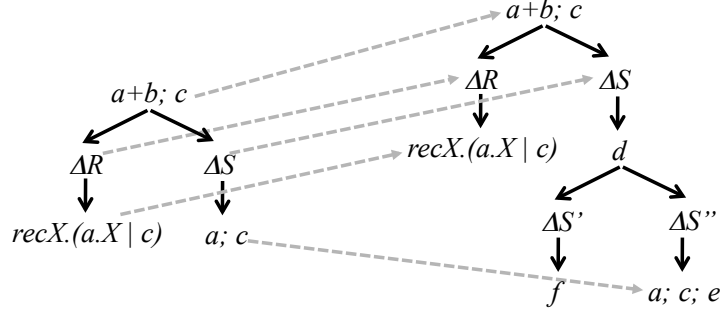


Fig. 3. Function φ shows that $P \preceq Q$ for P and Q of Example 5.24.

$\{\epsilon, 1, 1; 1, 2, 2; 1, 2; 1; 1, 2; 1; 1; 1, 2; 1; 2; 2; 1; 2; 1\}$. The injective function $\varphi = \{(\epsilon, \epsilon), (1, 1), (1; 1, 1; 1), (2, 2), (2; 1, 2; 1; 2; 1)\}$, depicted in Fig. 2, strictly preserves order inside trees.

Definition 5.23. Let $P, Q \in \mathcal{P}$. $P \preceq Q$ iff there exists an injective function φ from $\text{dom}(\text{TREE}(P))$ to $\text{dom}(\text{TREE}(Q))$ such that φ strictly preserves order inside trees and for every $\vec{n} \in \text{dom}(\varphi)$:

- either there exists $R \in \mathcal{P}$ such that $\text{TREE}(P)(\vec{n}) = \text{TREE}(Q)(\varphi(\vec{n})) = \Delta R$
- or $\text{TREE}(P)(\vec{n}), \text{TREE}(Q)(\varphi(\vec{n})) \in \mathcal{P}_{\text{seq}}^*$ and, if $\text{len}(\text{TREE}(P)(\vec{n})) > 0$, there exists an injective function f from $\{1, \dots, \text{len}(\text{TREE}(P)(\vec{n}))\}$ to $\{1, \dots, \text{len}(\text{TREE}(Q)(\varphi(\vec{n})))\}$ such that for every $i \in \text{dom}(f)$: $\text{TREE}(P)(\vec{n})_i = \text{TREE}(Q)(\varphi(\vec{n}))_{f(i)}$.

Notice that \preceq is a quasi-ordering in that it is obviously reflexive and it is immediate to verify, taking into account the two conditions for the injective function in the definition above, that it is transitive.

Example 5.24. Consider process $P = (a+b)|((\text{recX}.(a.X|c))\Delta R)|c|((a|c)\Delta S)$ of Example 5.20 and its associated tree t . Moreover consider process $Q = (a+b)|((\text{recX}.(a.X|c))\Delta R)|c|((d|(f\Delta S''))|(a|c|e)\Delta S')\Delta S$, for some processes R, S, S' and S'' , and its associated tree $t' = \{(\epsilon, a+b; c), (1, \Delta R), (1; 1, \text{recX}.(a.X|c)), (2, \Delta S), (2; 1, d), (2; 1; 1, \Delta S'), (2; 1; 1, f), (2; 1; 2, \Delta S''), (2; 1; 2; 1, a; c; e)\}$. The domain of t' is the same as in Example 5.22, hence the function $\varphi = \{(\epsilon, \epsilon), (1, 1), (1; 1, 1; 1), (2, 2), (2; 1, 2; 1; 2; 1)\}$ strictly preserves order inside trees. It is easy to observe (see Fig. 3) that φ also maps exception handlers into identical exception handlers and sequences of sequential terms into sequences of sequential terms which include a larger (multi)set of sequential terms, hence $P \preceq Q$.

We redefine on the transition system $(CCS_{\text{rec}}^{\Delta}, \mapsto)$ the function $\text{Deriv}(P)$ that associates to a process the set of its derivatives.

Definition 5.25. Let $P \in CCS_{\text{rec}}^{\Delta}$. With $\text{Deriv}(P)$ we denote the set of processes reachable from P with a sequence of reduction steps:

$$\text{Deriv}(P) = \{Q \mid P \mapsto^* Q\}$$

We are now ready to state our main result, that can be proved by simultaneously

exploiting Higman's Theorem on sequences (Higman 1952) (also known as Highman's Lemma) and Kruskal's Theorem on trees (Kruskal 1960).

Theorem 5.26. Let $P \in CCS_{rec}^\Delta$. Then the transition system $(Deriv(P), \mapsto, \preceq)$ is a finitely branching well-structured transition system with strong compatibility, decidable \preceq and computable *Succ*.

Proof. See Section 5.2.1. □

Corollary 5.27. Let $P \in CCS_{rec}^\Delta$. The termination of process P is decidable.

As replication is a particular case of recursion, we have that the same decidability result holds also for CCS_1^Δ .

5.2.1. *Proving Theorem 5.26* We first extend the definition of sequential terms, of interruption terms, and of $FPAR(P)$ to open terms. \mathcal{E}_{seq} is the subset of \mathcal{E} of terms P such that either $P = \mathbf{0}$ or $P = \alpha.P_1$ or $P = P_1 + P_2$ or $P = recX.P_1$, with $P_1, P_2 \in \mathcal{E}$. Let $\mathcal{E}_{int} = \{P\Delta Q \mid P, Q \in \mathcal{E}\}$. We extend the definition of $FPAR(P)$ to open terms $P \in \mathcal{E}$ by replacing the second clause in the definition of $FPAR(P)$ with:

$$FPAR(P) = P \text{ if } P \in \mathcal{E}_{seq} \cup \mathcal{E}_{int} \cup \{X \mid X \in Vars\}$$

where $Vars$ is the denumerable set of variables X in the syntax of \mathcal{E} terms.

A context is a term P_X of \mathcal{E} that includes a single occurrence of the free variable X (and possibly other free variables). A flat parallel context is a term P_X^i of \mathcal{E} such that P_X^i is a context and the sequence $w \in (\mathcal{E}_{int} \cup \{X\})^*$ obtained by filtering $FPAR(P_X^i)$ with respect to $\mathcal{E}_{int} \cup \{X\}$ is such that $w_i = X$.

Definition 5.28. Let $P \in \mathcal{P}$. We define the context tree of P , $CON_X(P)$, as the minimal tree $CON_X(P)$ over contexts P_X (and minimal auxiliary tree $CON_X^{odd}(P')$ over contexts P_X , with $P' \in \mathcal{P}_{int}$) satisfying

$$\begin{aligned} &(\epsilon, X) \in CON_X(P) \\ &(\vec{n}, P_X^i) \in CON_X^{odd}(P'), \text{ with } P' \in \mathcal{P}_{int}, \text{ implies } (i; \vec{n}, P_X^i\{P'/X\}) \in CON_X(P_X^i\{P'/X\}) \\ &(\epsilon, X) \in CON_X^{odd}(P'\Delta Q) \\ &(\vec{n}, P_X^i) \in CON_X(P') \text{ implies } (1; \vec{n}, P_X^i\Delta Q) \in CON_X^{odd}(P'\Delta Q) \end{aligned}$$

We define the subterm tree of P , $SUBT(P)$, as the tree satisfying the following condition. $(\vec{n}, P') \in SUBT(P)$ iff there exists a context P_X such that $(\vec{n}, P_X) \in CON_X(P)$ and P' is the term such that $P_X\{P'/X\} = P$. Similarly, $(\vec{n}, P') \in SUBT^{odd}(P)$, with $P \in \mathcal{P}_{int}$, iff there exists a context P_X such that $(\vec{n}, P_X) \in CON_X^{odd}(P)$ and P' is the term such that $P_X\{P'/X\} = P$.

Example 5.29. Consider process $P = (a+b)|((recX.(a.X|c))\Delta R)|c|((a|c)\Delta S)$ of Example 5.20. The tree $CON_X(P)$ is $\{(\epsilon, X), (1, (a+b)|X|c|((a|c)\Delta S)), (1; 1, (a+b)|(X\Delta R)|c|((a|c)\Delta S)), (2, (a+b)|((recX.(a.X|c))\Delta R)|c|X), (2; 1, (a+b)|((recX.(a.X|c))\Delta R)|c|$

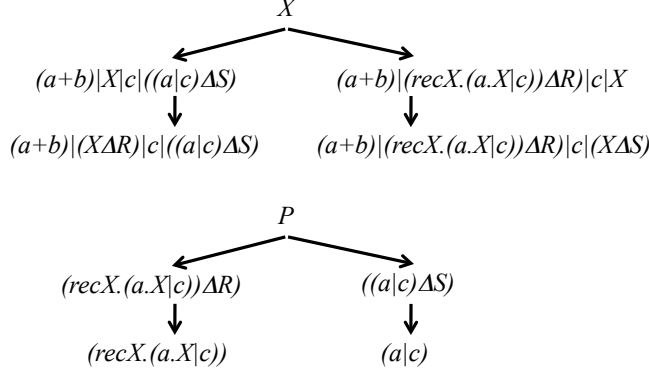


Fig. 4. $CON_X(P)$ and $SUBT(P)$ for the process $P = (a + b)|((recX.(a.X|c))\Delta R)|c|((a|c)\Delta S)$ of Example 5.29.

$(X\Delta S)\}$. The tree $SUBT(P)$ is $\{(\epsilon, P), (1, (recX.(a.X|c))\Delta R), (1; 1, recX.(a.X|c)), (2, (a|c)\Delta S), (2; 1, a|c)\}$. Both trees are depicted in Fig. 4.

In the following we will use $TREE^{\vec{n}}(P)$ to stand for $TREE(P)(\vec{n})$, $CON^{\vec{n}}(P)$ to stand for $CON(P)(\vec{n})$ and $SUBT^{\vec{n}}(P)$ to stand for $SUBT(P)(\vec{n})$.

Proposition 5.30. It holds that:

- $(\vec{n}, l) \in TREE(P)$ iff $(\vec{n}, P') \in SUBT(P)$ and we have: $l = FSEQ(P')$ if $len(\vec{n})$ is even or zero; $l = \Delta Q$, with $P' = P''\Delta Q$ for some P'' , if $len(\vec{n})$ is odd.
- $(\vec{n}, l) \in TREE^{odd}(P)$, with $P \in \mathcal{P}_{int}$, iff $(\vec{n}, P') \in SUBT^{odd}(P)$ and we have: $l = \Delta Q$, with $P' = P''\Delta Q$ for some P'' , if $len(\vec{n})$ is even or zero; $l = FSEQ(P')$ if $len(\vec{n})$ is odd.

Proof. First of all, notice that we have: $FINT(P)_i = P'$ iff there exists a flat parallel context P_X^i such that $P_X^i\{P'/X\} = P$ and $P' \in \mathcal{P}_{int}$. By exploiting this we can rewrite the second clause in the definition of $TREE(P)$ as

$$(\vec{n}, l) \in TREE^{odd}(P'), \text{ with } P' \in \mathcal{P}_{int}, \text{ implies } (i; \vec{n}, l) \in TREE(P_X^i\{P'/X\})$$

which makes it similar to the corresponding clause in the definition of $CON_X(P)$.

The statement is then trivially proved to hold by induction on $len(\vec{n})$, with $len(\vec{n}) = 0$ as the base case. \square

In the following we use $rchn(t)$ to denote the number of children of the root of a (non-empty) tree t . We have $rchn(t) = \max\{k | k \in dom(t)\}$.

Definition 5.31. Let $w, w' \in E^*$. We define the sequence obtained by inserting w' in w at position i , with $1 \leq i \leq len(w) + 1$, as the sequence w'' with length $len(w) + len(w')$ such that:

- $\forall 1 \leq n \leq i - 1. w''_n = w_n$
- $\forall 1 \leq n \leq len(w'). w''_{i-1+n} = w'_n$

— $\forall i \leq n \leq \text{len}(w). w''_{\text{len}(w')+n} = w_n$

Let t, t' be trees over E such that $(\epsilon, w) \in t$ and $(\epsilon, w') \in t'$ with $w, w' \in E'^* \subseteq E$; we define the tree obtained by inserting t' in t at position (i, j) , with $1 \leq i \leq \text{len}(w) + 1$ and $1 \leq j \leq \text{rchn}(t) + 1$, as the minimal tree t'' such that:

- $(\epsilon, w'') \in t''$ where w'' is obtained by inserting w' in w at position i
- $\forall \vec{n}, 1 \leq m \leq j - 1. (m; \vec{n}, l) \in t$ implies $(m; \vec{n}, l) \in t''$
- $\forall \vec{n}, 1 \leq m \leq \text{rchn}(t'). (m; \vec{n}, l) \in t'$ implies $(j - 1 + m; \vec{n}, l) \in t''$
- $\forall \vec{n}, j \leq m \leq \text{rchn}(t). (m; \vec{n}, l) \in t$ implies $(\text{rchn}(t') + m; \vec{n}, l) \in t''$

Finally, we define the sequence obtained from $w \in E^*$ by removing the i -th element, with $1 \leq i \leq \text{len}(w)$, written $w - i$, as the sequence $w' \in E^*$ such that $\text{len}(w') = \text{len}(w) - 1, \forall 1 \leq n \leq i - 1. w'_n = w_n$ and $\forall i + 1 \leq n \leq \text{len}(w). w'_{n-1} = w_n$.

We now prove that \preceq satisfies the strong compatibility property with respect to the transition system $(CCS_{rec}^\Delta, \xrightarrow{\alpha})$. The proof exploits the following lemma.

Lemma 5.32. $SUBT^{\vec{n}}(P) \xrightarrow{\alpha} P'$ implies $P \xrightarrow{\alpha} CON_X^{\vec{n}}(P)\{P'/X\}$

Proof. We show that:

- $SUBT^{\vec{n}}(P) \xrightarrow{\alpha} P'$ implies $P \xrightarrow{\alpha} CON_X^{\vec{n}}(P)\{P'/X\}$
- $SUBT^{odd}(P)(\vec{n}) \xrightarrow{\alpha} P'$ implies $P \xrightarrow{\alpha} CON_X^{odd}(P)(\vec{n})\{P'/X\}$

by induction on $\text{len}(\vec{n})$, where 0 is the base case. The inductive step is worked out as a trivial consequence of the fact that: $P \xrightarrow{\alpha} P'$ implies $P \Delta Q \xrightarrow{\alpha} P' \Delta Q$ and $P|Q \xrightarrow{\alpha} P'|Q$. \square

Theorem 5.33. Let $P, Q, P' \in \mathcal{P}$. If $P \xrightarrow{\alpha} P'$ and $P \preceq Q$ then there exists $Q' \in \mathcal{P}$ such that $Q \xrightarrow{\alpha} Q'$ and $P' \preceq Q'$.

Proof. The proof is by induction on $\text{depth}(TREE(P))/2$, where for any tree t we take $\text{depth}(t) = \max\{\text{len}(\vec{n}) | \vec{n} \in \text{dom}(t)\}$ and it is easy to see that for every $P \in \mathcal{P}$ we have that $\text{depth}(TREE(P))$ is even (because $\vec{n} \in \text{dom}(TREE(P))$ and $\text{len}(\vec{n})$ odd implies $\vec{n}; 1 \in \text{dom}(TREE(P))$).

Therefore, in the following, we prove the assertion for any P having a certain $\text{depth}(TREE(P))/2$: the base case of the induction is obtained by taking $\text{depth}(TREE(P))/2 = 0$.

Since $P \preceq Q$, there exists an injective strictly order-preserving φ such that the labels of $TREE(P)$ and $TREE(Q)$ are correctly related. In particular, we have that there exists an injection f from $\{1, \dots, \text{len}(TREE^\epsilon(P))\}$ to $\{1, \dots, \text{len}(TREE^{\varphi(\epsilon)}(Q))\}$ such that $TREE^\epsilon(P)_i = TREE^{\varphi(\epsilon)}(Q)_{f(i)}$. We have two cases:

- Either $P \xrightarrow{\alpha} P'$ is inferred from the move $P_1 \xrightarrow{\alpha} P'_1$ of a single process $P_1 \in FPAR(P)$.
- Or $\alpha = \tau$ and $P \xrightarrow{\alpha} P'$ is inferred from the moves $P_1 \xrightarrow{\bar{a}} P'_1$ and $P_2 \xrightarrow{a} P'_2$ of two processes $P_1, P_2 \in FPAR(P)$.

In the following we develop the first case; then we will show that the second one can be treated as a consequence of the first one. We have three subcases:

- 1 $P_1 \in FSEQ(P)$
- 2 $P_1 = P_2 \Delta P_3$, for some P_2, P_3 , and $P_1 \xrightarrow{\alpha} P'_1$ is inferred from a move $P_3 \xrightarrow{\alpha} P'_1$ of the process to the right-hand side of Δ .
- 3 $P_1 = P_2 \Delta P_3$, for some P_2, P_3 , and $P_1 \xrightarrow{\alpha} P'_1$ is inferred from a move $P_2 \xrightarrow{\alpha} P'_2$ of the process to the left-hand side of Δ .

where the second and third one can be obtained only if $depth(TREE(P))/2 > 0$.

The proof for the three subcases follows.

1. Let us assume that P_1 is the process at the j -th position in the sequence $FSEQ(P)$ and that the first process of \mathcal{P}_{int} to the right of P_1 in the sequence $FPAR(P)$ is at the k -th position in the sequence $FINT(P)$.

First of all we notice that $TREE(P')$ is obtained by inserting $TREE(P'_1)$ in the tree $TREE(P) - \{(\epsilon, FSEQ(P))\} \cup \{(\epsilon, FSEQ(P) - j)\}$ at position (j, k) .

From the existence of the function f above we derive $P_1 \in TREE^{\varphi(\epsilon)}(Q)$. Let $Q_1 = SUBT^{\varphi(\epsilon)}(Q)$. Let us assume that P_1 is the process at the j' -th position in the sequence $FSEQ(Q_1)$ and that the first process of \mathcal{P}_{int} to the right of P_1 in the sequence $FPAR(Q_1)$ is at the k' -th position in the sequence $FINT(Q_1)$. We have $Q_1 \xrightarrow{\alpha} Q'_1$, where $TREE(Q'_1)$ is obtained by inserting $TREE(P'_1)$ in the tree $TREE(Q_1) - \{(\epsilon, FSEQ(Q_1))\} \cup \{(\epsilon, FSEQ(Q_1) - j)\}$ at position (j', k') .

By using Lemma 5.32 we derive $Q \xrightarrow{\alpha} CON_X^{\varphi(\epsilon)}(Q)\{Q'_1/X\}$. Moreover, we consider $\varphi' =$

$$\begin{aligned} & \{(\epsilon, \varphi(\epsilon))\} \cup \\ & \{(m; \vec{n}, \varphi(\epsilon); z; \vec{n}') \mid 1 \leq m \leq k-1 \wedge \\ & \quad ((\varphi(\epsilon); z; \vec{n}' = \varphi(m; \vec{n}) \wedge \varphi(m; \vec{n}) \leq k'-1) \vee \\ & \quad (\varphi(\epsilon); z - (rchn(t'); \vec{n}' = \varphi(m; \vec{n}) \wedge \varphi(m; \vec{n}) \geq k')))\} \cup \\ & \{(k-1+m; \vec{n}, \varphi(\epsilon); k'-1+m; \vec{n}) \mid (m; \vec{n}) \in dom(t')\} \cup \\ & \{(rchn(t') + m; \vec{n}, \varphi(\epsilon); z; \vec{n}') \mid k \leq m \leq rchn(t) \wedge \\ & \quad ((\varphi(\epsilon); z; \vec{n}' = \varphi(m; \vec{n}) \wedge \varphi(m; \vec{n}) \leq k'-1) \vee \\ & \quad (\varphi(\epsilon); z - (rchn(t'); \vec{n}' = \varphi(m; \vec{n}) \wedge \varphi(m; \vec{n}) \geq k'))\}, \end{aligned}$$

where $t = TREE(P)$ and $t' = TREE(P'_1)$. φ' is a strictly order-preserving injection such that the labels of P' and $Q' = CON_X^{\varphi(\epsilon)}(Q)\{Q'_1/X\}$ are correctly related; hence $P' \preceq Q'$. In particular the existence of an injection f' from $\{1, \dots, len(TREE^\epsilon(P'))\}$ to $\{1, \dots, len(TREE^{\varphi(\epsilon)}(Q'))\}$ such that $TREE^\epsilon(P')_i = TREE^{\varphi(\epsilon)}(Q')_{f'(i)}$ derives from the existence of the injection f between the sequences labeling the nodes ϵ and $\varphi(\epsilon) = \varphi'(\epsilon)$ of P and Q : f' is obtained from f by removing the pair (j, j') (corresponding to the removal of P_1 from both sequences) and by simply accounting for the insertion of the same sequence $FSEQ(P'_1)$ in both sides. Notice that the preservation of the minimal common ancestor property holds because, when, in $TREE(P')$, $i; 1; \vec{n}$ nodes, with $k \leq i \leq k + rchn(TREE(P'))$, are involved and are considered together with nodes $j; 1; \vec{m}$ for some $j < k \vee j > k + rchn(TREE(P'))$, \vec{m} , the minimal common ancestor is the root ϵ . Moreover, the nodes $\varphi'(i; 1; \vec{n}) = \varphi'(\epsilon); i - k + k'; 1; \vec{n}$ and $\varphi'(j)$ have $\varphi'(\epsilon)$ as a minimal common ancestor, by construction of $TREE(Q')$ from $TREE(Q)$ and because the injection φ preserves the ancestor pre-order.

2. Let us assume that P_1 is the process at the i -th position in the sequence $FINT(P)$. We have $TREE^i(P) = TREE^{\varphi(i)}(Q) = \Delta P_3$; hence $SUBT^{\varphi(i)}(Q) = Q_1 \Delta P_3$ for some $Q_1 \in \mathcal{P}$ and $SUBT^{\varphi(i)}(Q) \xrightarrow{\alpha} P_3 \Delta \mathbf{0}$. By using Lemma 5.32 we derive $Q \xrightarrow{\alpha} CON_X^{\varphi(i)}(Q)\{P_1 \Delta \mathbf{0}/X\}$. Moreover, we consider $\varphi' = \varphi - \{(i; 1; \vec{n}, \vec{m}) | \vec{n}, \vec{m} \in \mathbf{N}^*\} \cup \{(i; 1; \vec{n}, \varphi(i); 1; \vec{n}) | \vec{n} \in dom(TREE(P_1))\}$. φ' is a strictly order-preserving injection such that the labels of P' and $Q' = CON_X^{\varphi(i)}(Q)\{P_1 \Delta \mathbf{0}/X\}$ are correctly related; hence $P' \preceq Q'$. Notice that the preservation of the minimal common ancestor property holds because, when, in $TREE(P')$, $i; 1; \vec{n}$ nodes are involved and are considered together with nodes $j; 1; \vec{m}$ for some $j \neq i, \vec{m}$, the minimal common ancestor is the root ϵ . Since the nodes i, j have ϵ as a minimal common ancestor, $\varphi(i)$ and $\varphi(j)$ must have $\varphi(\epsilon)$ as a minimal common ancestor. Moreover, since the injection φ preserves the ancestor pre-order and φ' differs from φ just over nodes $\varphi(i; 1; \vec{n}')$ for some \vec{n}' , $\varphi(i; 1; \vec{n})$ and $\varphi(j; 1; \vec{m})$ must have $\varphi(\epsilon)$ as a minimal common ancestor.
3. Let us assume that P_1 is the process at the i -th position in the sequence $FINT(P)$. We have that $\varphi' = \{(\vec{n}, \vec{m}) | \varphi(i; 1; \vec{n}) = \varphi(i; 1; \vec{m})\}$ (that makes sense because φ preserves the ancestor pre-order) is a strictly order-preserving injection such that the labels of P_2 and $SUBT_{\varphi(i;1)}(Q)$ are correctly related; hence $P_2 \preceq SUBT_{\varphi(i;1)}(Q)$. By applying the induction hypothesis we derive that there exists $Q'_1 \in \mathcal{P}$ such that $SUBT_{\varphi(i;1)}(Q) \xrightarrow{\alpha} Q'_1$ and $P'_2 \preceq Q'_1$. Hence, there exists a strictly order-preserving injection φ'' such that the labels of $TREE(P'_2)$ and $TREE(Q'_1)$ are correctly related. By using Lemma 5.32 we derive $Q \xrightarrow{\alpha} CON_X^{\varphi(i;1)}(Q)\{Q'_1/X\}$. Moreover, we consider $\varphi''' = \varphi - \{(i; 1; \vec{n}, \vec{m}) | \vec{n}, \vec{m} \in \mathbf{N}^*\} \cup \{(i; 1; \vec{n}, \varphi(i; 1; \vec{m}) | \varphi''(\vec{n}) = \vec{m}\}$. φ''' is a strictly order-preserving injection such that the labels of P' and $Q' = CON_X^{\varphi(i;1)}(Q)\{Q'_1/X\}$ are correctly related; hence $P' \preceq Q'$. Notice that the preservation of the minimal common ancestor property holds because, when, in $TREE(P')$, $i; 1; \vec{n}$ nodes are involved and are considered together with nodes $j; 1; \vec{m}$ for some $j \neq i, \vec{m}$, the minimal common ancestor is the root ϵ . Since the nodes i, j have ϵ as a minimal common ancestor, $\varphi(i)$ and $\varphi(j)$ must have $\varphi(\epsilon)$ as a minimal common ancestor. Moreover, since the injection φ preserves the ancestor pre-order and φ''' differs from φ just over nodes $\varphi(i; 1; \vec{n}')$ for some \vec{n}' , $\varphi(i; 1; \vec{n})$ and $\varphi(j; 1; \vec{m})$ must have $\varphi(\epsilon)$ as a minimal common ancestor.

The case $\alpha = \tau$ and $P \xrightarrow{\alpha} P'$ is inferred from the moves $P_1 \xrightarrow{\bar{\alpha}} P'_1$ and $P_2 \xrightarrow{\bar{\alpha}} P'_2$ of two processes P_1, P_2 at different positions in $FPAR(P)$. Let us suppose that P_1 is at position i in $FPAR(P)$ and that P_2 is at position j in $FPAR(P)$.

Consider the flat parallel context P_X^i such that $P_X^i\{P_1/X\} = P$ and P_X^j such that $P_X^j\{P_2/X\} = P$. We have that $P \xrightarrow{\bar{\alpha}} P_X^i\{P'_1/X\}$ and that $P \xrightarrow{\bar{\alpha}} P_X^j\{P'_2/X\}$. Therefore, by the same proof as in the previous case, there exists Q'' and Q''' such that $Q \xrightarrow{\bar{\alpha}} Q''$ and $Q \xrightarrow{\bar{\alpha}} Q'''$. Let us now observe that, no matter which of the three cases above for the inference of a move of P applies, in the proof above we identify Q_1 and Q_2 in $FPAR(SUBT^{\varphi(\epsilon)}(Q))$ such that $Q_1 \xrightarrow{\bar{\alpha}} Q'_1$ and $Q_2 \xrightarrow{\bar{\alpha}} Q'_2$. Moreover Q_1 and Q_2 are at different positions in $FPAR(SUBT^{\varphi(\epsilon)}(Q))$ because:

- if Q_1 and Q_2 are both \mathcal{P}_{seq} terms (subcase 1 above), then the injective function f yields Q_1 and Q_2 at different positions because P_1, P_2 are at different positions;
- if Q_1 and Q_2 are both \mathcal{P}_{int} terms (subcases 2 and 3) then we have that, called k the position of P_1 in $FINT(P)$ and h the position of P_2 in $FINT(P)$ (obviously $k \neq h$), Q_1 is the term at position k' , where $k'; \vec{n} = \varphi(k)$ for some \vec{n} , in $FINT(SUBT^{\varphi(\epsilon)}(Q))$ and Q_2 is the term at position h' , where $h'; \vec{m} = \varphi(h)$ for some \vec{m} , in $FINT(SUBT^{\varphi(\epsilon)}(Q))$ and we must have $k' \neq h'$ because otherwise φ would not preserve the minimal common ancestor of nodes k and h (that is the root ϵ).

For proving the assertion we need to use another property of the terms Q' and injective functions φ' (and related injective function f' relating $TREE^\epsilon(P')$ and $TREE^{\varphi(\epsilon)}(Q')$) showing that $P' \preceq Q'$ built in the previous case (subcases 1, 2 and 3). Consider a term in $FSEQ(P)$ that is not P_1 and let z be its position in $FSEQ(P)$. If m is the position that such a term assumes in $FSEQ(P_X^i\{P_1/X\})$ then $f'(m)$ is the position that the $f(z)$ -th term in $FSEQ(SUBT^{\varphi(\epsilon)}(Q))$ (that is not Q_1 because f is injective) assumes in $FSEQ(Q_X^{i'}\{Q_1/X\})$, where $Q_X^{i'}$ such that $Q = CON_X^{\varphi(\epsilon)}(Q)\{Q_X^{i'}\{Q_1/X\}/X\}$. Similarly, consider a term in $FINT(P)$ that is not P_1 and let z be its position in $FINT(P)$. If m is the position that such a term assumes in $FINT(P_X^i\{P_1/X\})$ then m' such that $m'; \vec{n} = \varphi'(m)$, for some \vec{n} , is the position that the z' -th term in $FINT(SUBT^{\varphi(\epsilon)}(Q))$, with $z'; \vec{n} = \varphi(z)$ (that is not Q_1 because φ is injective and preserves the minimal common ancestor), assumes in $FINT(Q_X^{i'}\{Q_1/X\})$.

Since the two processes P_1, P_2 are at different positions in $FPAR(P)$, then $P \xrightarrow{\vec{a}} P_X^i\{P_1/X\} \xrightarrow{\vec{a}} P'$, where the two moves are inferred from the moves $P_1 \xrightarrow{\vec{a}} P_1'$ and $P_2 \xrightarrow{\vec{a}} P_2'$, respectively. From the first move, by building a corresponding move for Q to Q' and an injective function φ' (and related injective function f' relating $TREE^\epsilon(P')$ and $TREE^{\varphi(\epsilon)}(Q')$) as in the previous case, we have $Q \xrightarrow{\vec{a}} Q' \equiv CON_X^{\varphi(\epsilon)}(Q)\{Q_X^{i'}\{Q_1/X\}/X\}$, inferred from a move $Q_1 \xrightarrow{\vec{a}} Q_1'$ of Q_1 . Moreover, if $P_2 \in \mathcal{P}_{seq}$ and m is the position that term P_2 (that is inside P_X^i) assumes in $FSEQ(P_X^i\{P_1/X\})$ then $f'(m)$ is the position that Q_2 (that is inside $Q_X^{i'}$) assumes in $FSEQ(Q_X^{i'}\{Q_1/X\})$. If, instead, $P_2 \in \mathcal{P}_{int}$ and m is the position term P_2 (that is inside P_X^i) assumes in $FINT(P_X^i\{P_1/X\})$ then m' such that $m'; \vec{n} = \varphi'(m)$, for some \vec{n} , is the position that Q_2 (that is inside $Q_X^{i'}$) assumes in $FINT(Q_X^{i'}\{Q_1/X\})$. From the second move, we have that we can similarly build a move from $CON_X^{\varphi(\epsilon)}(Q)\{Q_X^{i'}\{Q_1/X\}/X\}$ to $CON_X^{\varphi(\epsilon)}(Q)\{Q''/X\}$, that is inferred from a move $Q_2 \xrightarrow{\vec{a}} Q_2'$ of Q_2 (because of the correspondence between P_2 and Q_2 in φ' detailed above), and an injective function φ'' showing that $P' \preceq CON_X^{\varphi(\epsilon)}(Q)\{Q''/X\}$. Therefore, since Q_1 and Q_2 are in different positions in $FPAR(SUBT^{\varphi(\epsilon)}(Q))$, then $SUBT^{\varphi(\epsilon)}(Q) \xrightarrow{\vec{\tau}} Q''$; hence, by Lemma 5.32, $Q \xrightarrow{\vec{\tau}} CON_X^{\varphi(\epsilon)}(Q)\{Q''/X\}$. \square

In order to prove that \preceq is a wqo. we exploit both Higman's Theorem that allows us to lift a wqo on a set \mathcal{S} to a corresponding wqo on \mathcal{S}^* , i.e. the set of finite sequences on \mathcal{S} , and Kruskal's Tree Theorem that allows us to lift a wqo on a set \mathcal{S} to a corresponding wqo on trees over \mathcal{S} .

Definition 5.34. Let \mathcal{S} be a set and \leq a wqo over \mathcal{S} . The relation \leq_* over \mathcal{S}^* is defined as follows. Let $t, u \in \mathcal{S}^*$, with $t = t_1 t_2 \dots t_m$ and $u = u_1 u_2 \dots u_n$. We have that $t \leq_* u$ iff there exists an injection f from $\{1, 2, \dots, m\}$ to $\{1, 2, \dots, n\}$ such that $t_i \leq u_{f(i)}$ and $1 \leq f(1) < \dots < f(m) \leq n$.

Note that relation \leq_* is a quasi-ordering over \mathcal{S}^* .

Theorem 5.35. [Higman] Let \mathcal{S} be a set and \leq a wqo over \mathcal{S} . Then, the relation \leq_* is a wqo over \mathcal{S}^* .

Definition 5.36. Let \mathcal{S} be a set and \leq a wqo over \mathcal{S} . The relation \leq^{tree} on the set of trees over \mathcal{S} is defined as follows. Let t, u be trees over \mathcal{S} . We have that $t \leq^{tree} u$ iff there exists a strictly order-preserving injection φ from $dom(t)$ to $dom(u)$ such that for every $\vec{n} \in dom(\varphi)$ we have that $t(\vec{n}) \leq u(\varphi(\vec{n}))$.

Note that relation \leq^{tree} is a quasi-ordering for the set of trees over \mathcal{S} .

Theorem 5.37. [Kruskal] Let \mathcal{S} be a set and \leq a wqo over \mathcal{S} . Then, the relation \leq^{tree} is a wqo on the set of trees over \mathcal{S} .

Moreover, the following trivial proposition will be used to show that \preceq is a wqo.

Proposition 5.38. Let \mathcal{S} be a finite set. Then equality is a wqo over \mathcal{S} .

In order to apply Kruskal, we need first to define a wqo on the set of labels of the trees associated to the derivatives of a process P . To prove this result, we show that these labels are sequences of elements taken from a finite domain (then we can apply Proposition 5.38 and Theorem 5.35). We first introduce some auxiliary notation.

Given a process $P \in CCS_{rec}^\Delta$, we define the set $\mathcal{P}_{seq}(P)$ of the sequential subprocesses of P and the set $\mathcal{P}_{inth}(P)$ of the interruption handlers that are included in P as follows:

$$\begin{aligned} \mathcal{P}_{seq}(P) &= \{Q \in \mathcal{P}_{seq} \mid Q \in ClSub(P)\} \\ \mathcal{P}_{inth}(P) &= \{\Delta Q \in \mathcal{P}_{inth} \mid \exists R \in \mathcal{P} : R \Delta Q \in ClSub(P)\} \end{aligned}$$

where $ClSub(P)$ is the set of the closures of the subterms of P , i.e. $P' \in ClSub(P)$ if the closed term P' is obtained from a subterm P'' of P by replacing each free variable in P'' with the corresponding binding recursion inside P . For instance, if $P = recX.a.recY.(b.X + c.Y)$ we have that, e.g., $b.(recX.a.recY.(b.X + c.Y)) + c.recY.(b.(recX.a.recY.(b.X + c.Y)) + c.Y)$ is in $ClSub(P)$ in that it is obtained as the closure of the subterm $b.X + c.Y$ of P . Note that, obviously, (since we always consider variable definitions as they appear inside P that does not change during the replacements) the order of variable replacement is not important given that we replace variables until we reach a closed term: if we replace variable definitions considering them from inner to outer scopes, i.e. the definition of Y before the definition of X in the example above, we do not have to repeat replacements for the same variable.

It is immediate to observe that, since subterms of a term are finitely many and for each term there is exactly one closure of it, both $\mathcal{P}_{seq}(P)$ and $\mathcal{P}_{inth}(P)$ are always finite.

Proposition 5.39. Given a process $P \in CCS_{rec}^\Delta$, the sets $\mathcal{P}_{seq}(P)$ and $\mathcal{P}_{inth}(P)$ are finite.

Proof. By induction on the structure of P . □

We now define the set \mathcal{P}_P of those processes whose sequential subprocesses and interruption handlers either occur in P or are $\mathbf{0}$ and $\Delta\mathbf{0}$, respectively. The latter is needed to include terms derived from P due to the (modified) semantics of the Δ operator (see the termination preserving transformation of the semantics at the beginning of Section 5).

Definition 5.40. Let P be a process of CCS_{rec}^Δ . With \mathcal{P}_P we denote the set of CCS_{rec}^Δ processes defined by

$$\mathcal{P}_P = \{Q \in CCS_{rec}^\Delta \mid \mathcal{P}_{seq}(Q) \subseteq (\mathcal{P}_{seq}(P) \cup \{\mathbf{0}\}) \wedge \mathcal{P}_{inth}(Q) \subseteq (\mathcal{P}_{inth}(P) \cup \{\Delta\mathbf{0}\})\}$$

Note that for every P , the set \mathcal{P}_P is infinite as there are no restrictions on the number of instances of the sequential subprocesses or of the interrupts. Nevertheless, we have that \preceq is a wqo on the set \mathcal{P}_P :

Theorem 5.41. Let $P \in CCS_{rec}^\Delta$. The relation \preceq is a wqo over \mathcal{P}_P .

Proof. We prove the theorem showing the existence of a wqo \sqsubseteq such that for every $Q, R \in \mathcal{P}_P$, if $Q \sqsubseteq R$ then also $Q \preceq R$.

The new relation \sqsubseteq is defined as follows. Given $Q, R \in \mathcal{P}_P$, we have that $Q \sqsubseteq R$ if and only if $TREE(Q) (=_{*})^{tree} TREE(R)$ where $(=_{*})^{tree}$ is a relation on trees obtained from the identity over $\mathcal{P}_{seq}(P) \cup \mathcal{P}_{inth}(P)$ lifted to sequences according to Definition 5.34, and then lifted to trees according to Definition 5.36. As $\mathcal{P}_{seq}(P) \cup \mathcal{P}_{inth}(P)$ is finite, we have that $=_{*}$ is a wqo (Proposition 5.38 and Higman's Theorem 5.35). Thus, by Kruskal's Theorem 5.37, we have that also $(=_{*})^{tree}$ is a wqo.

It remains to prove that \sqsubseteq implies \preceq . To prove this, we show that given $Q \sqsubseteq R$ then also the conditions reported in the Definition 5.23 are satisfied by Q and R ; thus also $Q \preceq R$. Let us consider $Q \sqsubseteq R$; then there exists an order preserving injective function φ from $dom(TREE(Q))$ to $dom(TREE(R))$ such that for every $\vec{n} \in dom(\varphi)$ we have that $TREE(Q)(\vec{n}) =_{*} TREE(R)(\varphi(\vec{n}))$. There are two possible cases. Either $TREE(Q)(\vec{n}) \in \mathcal{P}_{inth}$ or $TREE(Q)(\vec{n}) \in \mathcal{P}_{seq}^*$.

- In the first case we have that $TREE(Q)(\vec{n}) = TREE(R)(\varphi(\vec{n})) = \Delta S$ for some $S \in \mathcal{P}$ (thus the first item of Definition 5.23 holds).
- In the second case we have that $TREE(Q)(\vec{n}) =_{*} TREE(R)(\varphi(\vec{n}))$, i.e., there exists an injective function f from $\{1, \dots, len(TREE(Q)(\vec{n}))\}$ to $\{1, \dots, len(TREE(R)(\varphi(\vec{n})))\}$ such that $1 \leq f(1) < \dots < f(len(TREE(Q)(\vec{n}))) \leq len(TREE(R)(\varphi(\vec{n})))$ and $TREE(Q)(\vec{n})_i = TREE(R)(\varphi(\vec{n}))_{f(i)}$ (thus the second item of Definition 5.23, where we simply require that f is injective, holds).

□

In order to prove that $(CCS_{rec}^\Delta, \mapsto)$ is a well-structured transition system we simply have to show that for every P the derivatives of P , i.e. $Deriv(P)$, is a subset of \mathcal{P}_P . This follows from the following proposition.

Proposition 5.42. Let $P \in CCS_{rec}^\Delta$ and $Q \in \mathcal{P}_P$. If $Q \xrightarrow{\alpha} Q'$ then $Q' \in \mathcal{P}_P$.

Proof. By induction on the proof of transition $Q \xrightarrow{\alpha} Q'$. □

Corollary 5.43. Let $P \in CCS_{rec}^\Delta$. We have that $Deriv(P) \subseteq \mathcal{P}_P$.

We now complete the proof of decidability of termination in $(CCS_{rec}^\Delta, \xrightarrow{\alpha})$.

Proof. of Theorem 5.26 The fact that $(Deriv(P), \xrightarrow{\alpha})$ is finitely branching derives from an inspection of the transition rules taking into account the weak guardedness constraint in recursions. Strong compatibility was proved in Theorem 5.33. The fact that \preceq is a wqo on $Deriv(P)$ is a consequence of Corollary 5.43 and Theorem 5.41. □

6. Interpretation of the results

A first comment concerns the computational power of the considered calculi. In the proof of the undecidability results we have considered RAMs which is a Turing-complete formalism in the classical setting, i.e., given a partial recursive function there is a corresponding RAM program which computes it. This means that whenever the function is defined, the corresponding RAM program is guaranteed to complete its computation yielding the correct value.

We have shown that it is possible to encode deterministically any RAM in CCS_{rec}^{tc} ; thus we can conclude that the calculus is *Turing complete* according to the following criterion.

- Given a partial recursive function with a given input there is a corresponding process such that:
 - if the function is defined for the input then all computations of the process terminate and make the corresponding output available (it is sufficient to count the nesting depth of the try-catch operator in the subprocess of the final process that represents the output register);
 - if the function is not defined for the input then all computations of the process do not terminate.

For the other considered calculi, the decidability of universal termination allows us to conclude that the calculi are not Turing complete according to the above criterion. Nevertheless, in the proof of the undecidability of existential termination in these calculi, we show that RAMs can be encoded in such a way that at least the terminating computations respect RAM computations. We can conclude that these calculi satisfy a weaker criterion.

- Given a partial recursive function with a given input there is a corresponding process such that:
 - if the function is defined for the input then there exists at least one computation that terminates and moreover all computations that terminate make the corresponding output available;

- if the function is not defined for the input then all computations of the process do not terminate.

The difference with respect to the above criterion for Turing completeness is in the first item: when the function is defined, the corresponding process may have some computations that do not terminate. We can refer to this weaker criterion for Turing universality as *weak Turing completeness*. We can conclude that CCS_{rec}^{tc} is Turing complete while CCS_1^Δ , CCS_1^{tc} , and CCS_{rec}^Δ are only weakly Turing complete.

We now compare the computational strength of the interrupt and of the try-catch operators with respect to calculi in which such operators are not present. In particular we consider a fragment of CCS (called *GCCS* in the following) obtained removing restriction, relabeling, and assuming that the operands of a choice are always prefixed processes as in $\alpha_1.P_1 + \alpha_2.P_2$. It is well known that the processes of *GCCS* can be translated into strongly bisimilar finite Petri-nets (Goltz 1988). As existential and universal termination are decidable for finite Petri-nets (see, e.g., (Esparza and Nielsen 1994) for a survey about decidable properties in Petri-nets), and because strong bisimilarity preserves both existential and universal termination (i.e., an existentially —resp. universally— terminating process cannot be strongly bisimilar to a non existentially —resp. non universally— terminating process), we can conclude that both termination problems are decidable in *GCCS* (in fact, it is sufficient to check universal —resp. universal— termination on the Petri-nets obtained by removing all transitions different from those representing τ actions). Thus, *GCCS* is not weakly Turing complete. As all the encodings of RAMs that we have presented in this paper exploit only guarded choice, we can conclude that if we extend *GCCS* with the interrupt operator we obtain a weakly Turing complete calculus, and if we extend *GCCS* with try-catch we reach Turing completeness. In the light of this last observation we can conclude that:

- there exists no computable encoding of the interrupt operator in *GCCS* that preserves existential termination;
- there exists no computable encoding of the try-catch operator in *GCCS* that preserves either existential or universal termination;
- there exists no computable encoding of the try-catch operator in *GCCS* extended with the interrupt operator that preserves universal termination.

The last item formalizes an interesting impossibility result about the encodability of try-catch into the interrupt operator. As far as the inverse encoding is concerned, one may think to model the interrupt operator in terms of try-catch by using an encoding like:

$$[[P\Delta Q]] = \begin{cases} \text{try}(P|\text{throw})\text{catch } Q & \text{if } Q \text{ can execute at least one action} \\ P & \text{otherwise} \end{cases}$$

It is easy to see that given a process P including the interrupt operator, the corresponding encoding reproduces all the computations of P . Thus, we have that if P existentially terminates, then also its encoding existentially terminates. Nevertheless, the encoding does not preserve existential termination because the opposite implication does not hold.

Consider, for instance, the process:

$$a.recX.(\tau.X) \mid b.\bar{c} \mid (\bar{a}.\bar{b})\Delta c$$

that does not existentially terminate. According to the approach followed by the previous encoding, this process is modeled by:

$$a.recX.(\tau.X) \mid b.\bar{c} \mid \mathbf{try}(\bar{a}.\bar{b})\mathbf{throw} \mathbf{catch} c$$

which instead existentially terminates in case the **throw** action is the first one to be executed. We leave for future work the investigation of a faithful encoding of the interrupt operator in terms of try-catch.

7. Conclusion and Related Work

We have investigated the impact of the interrupt and the try-catch operators on the decidability of existential and universal termination in fragments of CCS with either replication or recursion. Table 1 in the Introduction depicts all the results proved in this paper and the impact of these results has been discussed in Section 6.

It is worth comparing the results proved in this paper with similar results presented in (Busi *et al.* 2003; Busi *et al.* 2008). In that paper, the interplay between replication/recursion and restriction is studied: a fragment of CCS with restriction and replication is proved to be weakly Turing powerful (according to the criteria presented in the previous section), while the corresponding fragment with recursion is proved to be Turing complete. The main result proved in (Busi *et al.* 2003; Busi *et al.* 2008) is the decidability of universal termination in CCS with replication instead of recursion. In that paper, general replication $!P$ is considered instead of the more constrained guarded replication $!a.P$ considered in this paper. We can generalize the results, proved in this paper for CCS_{\dagger}^{Δ} and CCS_{\dagger}^{tc} , considering general replication instead of guarded replication. The undecidability results trivially hold moving from guarded to general replication. As far as the decidability results are concerned, we now show how to modify the proof in Section 5.1 in order to prove decidability of universal termination also in the case of general replication. As we resort to the theory of well-structured transition systems, we need to consider a finitely branching transition system. To do so, we do the proof on an alternative finitely branching transition system obtained considering the following rules for replication:

$$\frac{P \xrightarrow{\alpha} P'}{!P \xrightarrow{\alpha} P' \mid !P} \qquad \frac{P \xrightarrow{\alpha} P' \quad P \xrightarrow{\bar{\alpha}} P''}{!P \xrightarrow{\tau} P' \mid P'' \mid !P}$$

which are equivalent, with respect to universal termination (see the proof in (Busi *et al.*

2003; Busi *et al.* 2008)), to the usual semantics:

$$\frac{P|!P \xrightarrow{\alpha} P'}{!P \xrightarrow{\alpha} P'}$$

As far as the try-catch operator is concerned, the proof in Section 5.1 applies also to general replication (defined according to the finitely branching rules) substituting $!\alpha.P$ with $!P$ in the definition of $d_{tc}(-)$ and $Sub(-)$. As far as the interrupt operator is concerned, we need also to replace all occurrences of $\text{try } P \text{ catch } Q$ by $P\Delta Q$.

The interplay between restriction and replication/recursion observed in (Busi *et al.* 2003; Busi *et al.* 2008) is similar to what we have proved in this paper about the interplay between the try-catch operator and replication/recursion. This proves a strong connection between restriction and try-catch, at least as far as the computational power is concerned. Intuitively, this follows from the fact that, similarly to restriction, the try-catch operator defines a new scope for the special **throw** action which is bound to a specific exception handler. On the contrary, the interrupt operator does not have the same computational power. In fact, the calculus with recursion and interrupt is only weakly Turing powerful. This follows from the fact that this operator does not provide a similar binding mechanism between the interrupt signals and the interruptible processes.

It is worth comparing our criterion for the evaluation of the expressive power with the criterion used by Palamidessi in (Palamidessi 2003) to distinguish the expressive power of the synchronous and the asynchronous π -calculus. Namely, in that paper, it is proved that there exists no modular embedding of the synchronous into the asynchronous π -calculus that preserves any reasonable semantics. When we prove that universal termination (resp. existential termination) is undecidable in one calculus while it is not in another one, we also prove that there exists no computable encoding (thus also no modular embedding) of the former calculus into the latter that preserves any semantics preserving universal termination (resp. existential termination). If we assume that the termination of one computation is observable (as done for instance in process calculi with explicit termination (Baeten *et al.* 2008)), we have that any reasonable semantics (according to the notion of reasonable semantics presented in (Palamidessi 2003)) preserves both universal and existential termination.

We conclude by mentioning the investigation of the expressive power of the disrupt operator (similar to our interrupt operator) done by Baeten and Bergstra in a technical report (Baeten and Bergstra 2000). In that paper, a different notion of expressive power is considered: a calculus is more expressive than another one if it generates a larger set of transition systems. We consider a stronger notion of expressive power: a calculus is more expressive than another one if it supports a more faithful modeling of Turing complete formalisms.

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References

- J.C.M. Baeten, T. Basten, and M.A. Reniers. *Process algebra (equational theories of communicating processes)*. Cambridge Tracts in Theoretical Computer Science. Cambridge University Press, 2008.
- J.C.M. Baeten and J.A. Bergstra. Mode transfer in process algebra. Report CSR 00-01, Technische Universiteit Eindhoven. This paper is an expanded and revised version of J. Bergstra, A mode transfer operator in process algebra, Report P8808, Programming Research Group, University of Amsterdam, 2000. Available at <http://alexandria.tue.nl/extra1/wskrap/publichtml/200010731.pdf>
- L. Bocchi, C. Laneve, and G. Zavattaro. A calculus for long running transactions. In *FMOODS'03: Proceedings of the 6th IFIP International Conference on Formal Methods for Open Object-based Distributed Systems*, volume 2884 of *LNCS*, pages 124–138. Springer, 2003.
- M. Boreale, R. Bruni, L. Caires, R. De Nicola, I. Lanese, M. Loreti, F. Martins, U. Montanari, A. Ravara, D. Sangiorgi, V.T. Vasconcelos, and G. Zavattaro. SCC: A Service Centered Calculus. In *WS-FM 2006: Proceedings of the Third International Workshop on Web Services and Formal Methods*, volume 4184 of *LNCS*, pages 38–57. Springer, 2006.
- R. Bruni, H.C. Melgratti, and U. Montanari. Nested Commits for Mobile Calculi: Extending Join. In *TCS 2004: IFIP 18th World Computer Congress, TC1 3rd International Conference on Theoretical Computer Science*, pages 563–576. Kluwer, 2004.
- R. Bruni, H.C. Melgratti, and U. Montanari. Theoretical foundations for compensations in flow composition languages. In *POPL 2005: Proceedings of the 32nd Symposium on Principles of Programming Languages*, pages 209–220, ACM Press, 2005.
- N. Busi, M. Gabbrielli, and G. Zavattaro. Replication vs. Recursive Definitions in Channel Based Calculi. In *ICALP'03: Proceedings of 30th International Colloquium on Automata, Languages and Programming*, volume 2719 of *LNCS*, pages 133–144, Springer, 2003.
- N. Busi, M. Gabbrielli, and G. Zavattaro. On the Expressive Power of Recursion, Replication, and Iteration in Process Calculi. In *Mathematical Structure in Computer Science*, to appear. Extended version of (Busi *et al.* 2003).
- M. Butler and C. Ferreira. An operational semantics for StAC, a language for modelling long-running business transactions. In *COORDINATION'04: Proceedings of the 6th International Conference on Coordination Models and Languages*, volume 2949 of *LNCS*, pages 87–104. Springer, 2004.
- M. Butler, C.A.R. Hoare, and C. Ferreira. A trace semantics for long-running transactions. In *Proceedings of 25 Years of Communicating Sequential Processes*, volume 3525 of *LNCS*, pages 133–150. Springer, 2005.
- J. Esparza and M. Nielsen. Decidability Issues for Petri Nets—a Survey. *Bulletin of the European Association for TCS*, 52:245–262, 1994.
- A. Finkel and P. Schnoebelen. Well-Structured Transition Systems Everywhere!. *Theoretical Computer Science*, 256:63–92, 2001.
- U. Goltz. On Representing CCS Programs by Finite Petri Nets. In *MFCS'88: Proceedings of Mathematical Foundations of Computer Science*, volume 324 of *LNCS*, pages 339–350. Springer, 1988.

- C. Guidi, I. Lanese, F. Montesi, and G. Zavattaro. On the Interplay between Fault Handling and Request-Response Service Invocations. In *ACSD'08: Proceedings of 5th IEEE 8th International Conference on Application of Concurrency to System Design*, pages 190–199. IEEE Computer Society press, 2008.
- G. Higman. Ordering by divisibility in abstract algebras. In *Proc. London Math. Soc.*, volume 2, pages 236–366, 1952.
- C.A.R. Hoare. *Communicating Sequential Processes*. Prentice-Hall, 1985.
- J.B. Kruskal. Well-Quasi-Ordering, The Tree Theorem, and Vazsonyi's Conjecture. *Transactions of the American Mathematical Society*, 95(2): 210–225, 1960.
- C. Laneve and G. Zavattaro. Foundations of web transactions. In *FOSSACS 2005: Proceedings of the 8th International Conference on Foundations of Software Science and Computational Structures*, volume 3441 of *LNCS*, pages 282–298. Springer, 2005.
- A. Lapadula, R. Pugliese, and F. Tiezzi. A Calculus for Orchestration of Web Services. In *ESOP 2007: Proceedings of 16th European Symposium on Programming*, volume 4421 of *LNCS*, pages 33–47. Springer, 2007.
- R. Milner. *Communication and Concurrency*. Prentice-Hall, 1989.
- R. Milner, J. Parrow, and D. Walker. A Calculus of Mobile Processes, Part I + II. *Information and Computation*, 100(1):1–77, 1992.
- M.L. Minsky. *Computation: finite and infinite machines*. Prentice-Hall, Englewood Cliffs, 1967.
- J. Misra and W. R. Cook. Computation Orchestration. *Journal of Software and System Modeling*, 6(1): 83–110, 2007.
- OASIS. *WS-BPEL: Web Services Business Process Execution Language Version 2.0*. Technical report, OASIS, 2003.
- C. Palamidessi. Comparing the Expressive Power of the Synchronous and the Asynchronous pi-calculus. *Mathematical Structures in Computer Science*, 13(5): 685–719, Cambridge University Press, 2003. A short version of this paper appeared in POPL'97.
- J.C. Shepherdson and J. E. Sturgis. Computability of recursive functions. *Journal of the ACM*, 10:217–255, 1963.
- S.G. Simpson. Nonprovability of certain combinatorial properties of finite trees. In *Harvey Friedman's Research on the Foundations of Mathematics*, pages 87–117. North-Holland, 1985.
- H.T. Vieira, L. Caires, and J.C. Seco. The Conversation Calculus: A Model of Service-Oriented Computation. In *ESOP 2008: Proceedings of 17th European Symposium on Programming*, volume 4960 of *LNCS*, pages 269–283. Springer, 2008.
- W3C. *WS-CDL: Web Services Choreography Description Language*. Technical report, W3C, 2004.